

6721764

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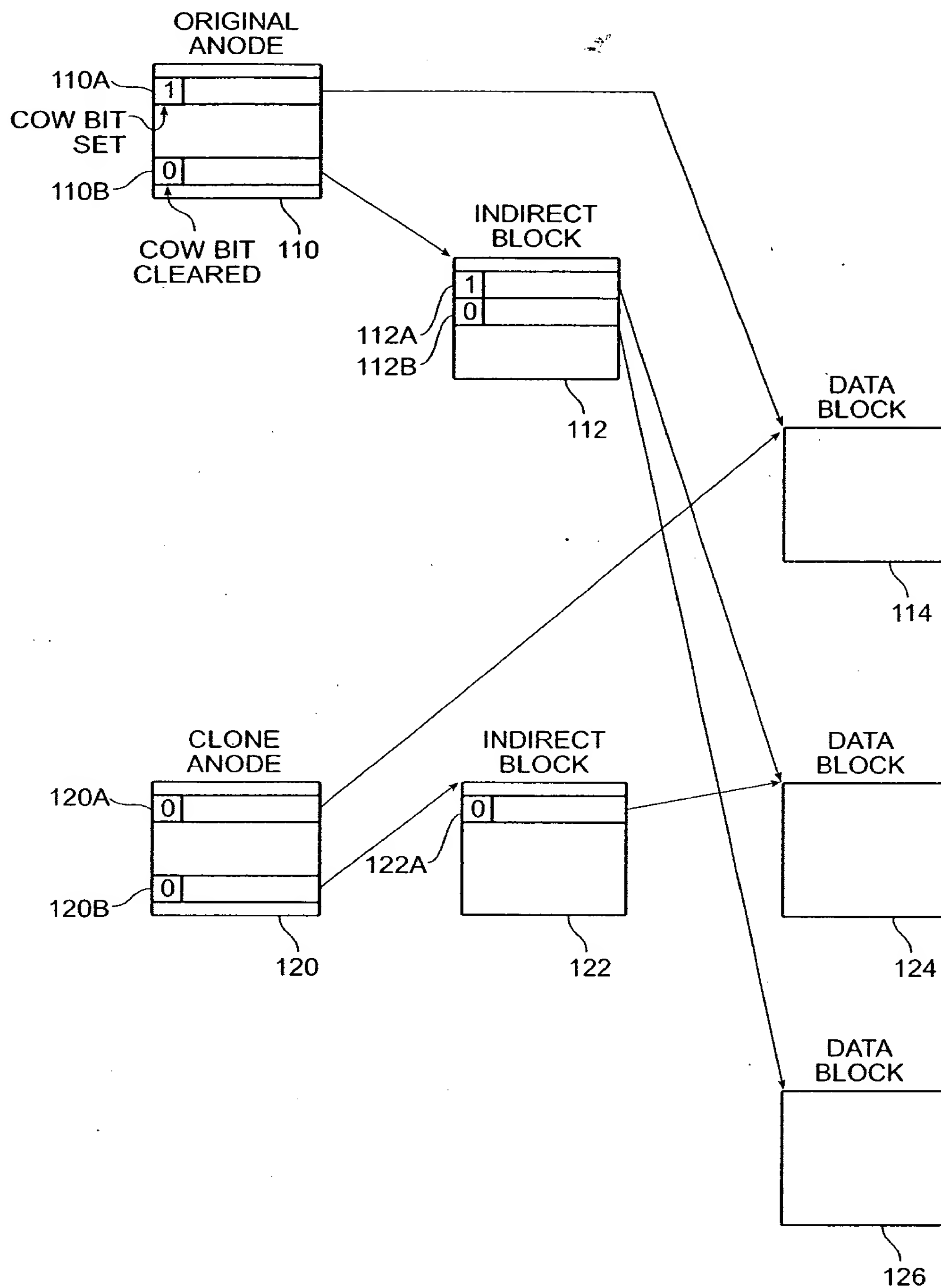


FIG. 1
PRIOR ART

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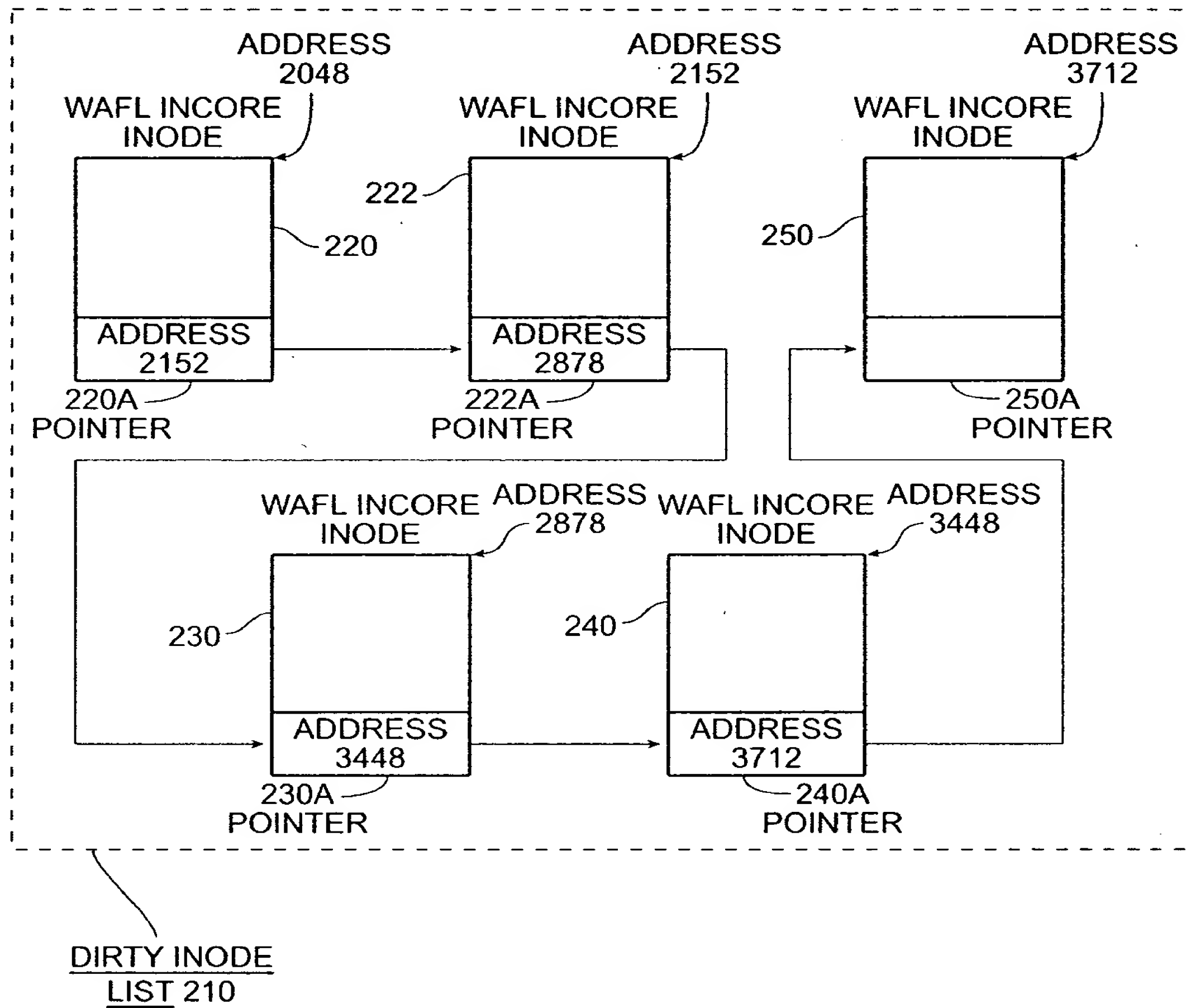


FIG. 2

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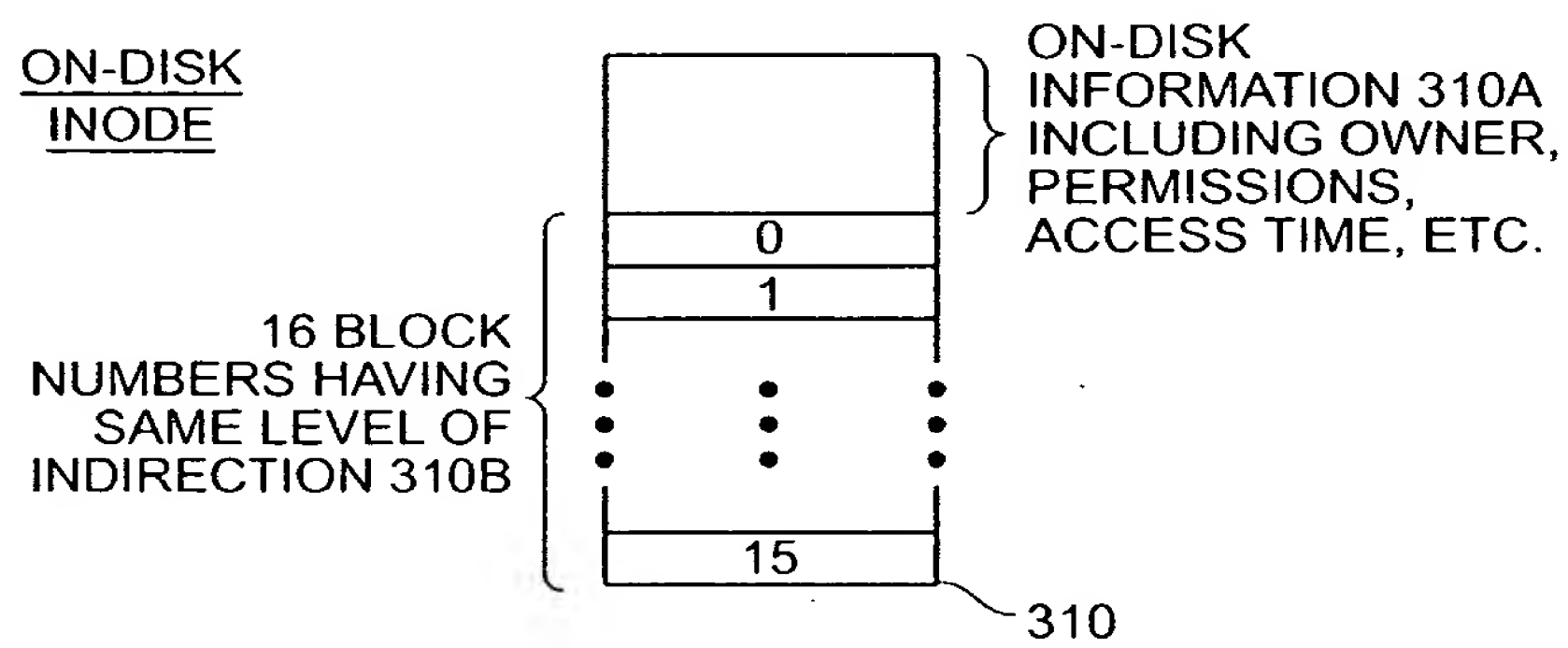


FIG. 3

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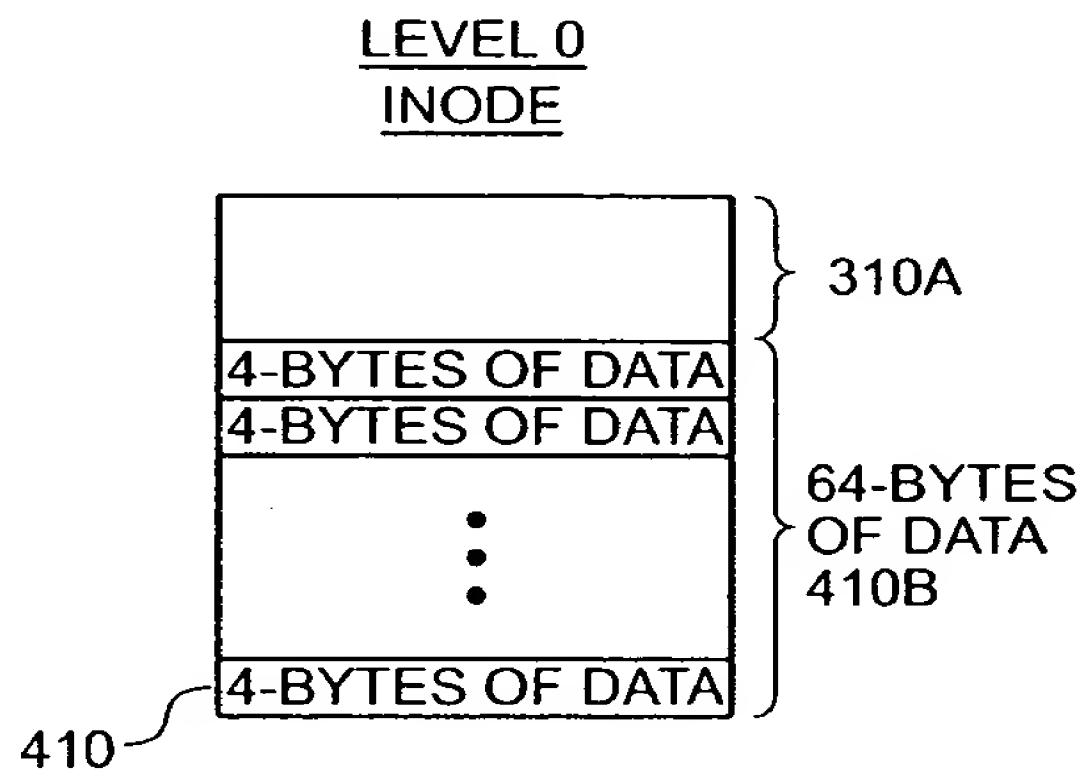


FIG. 4A

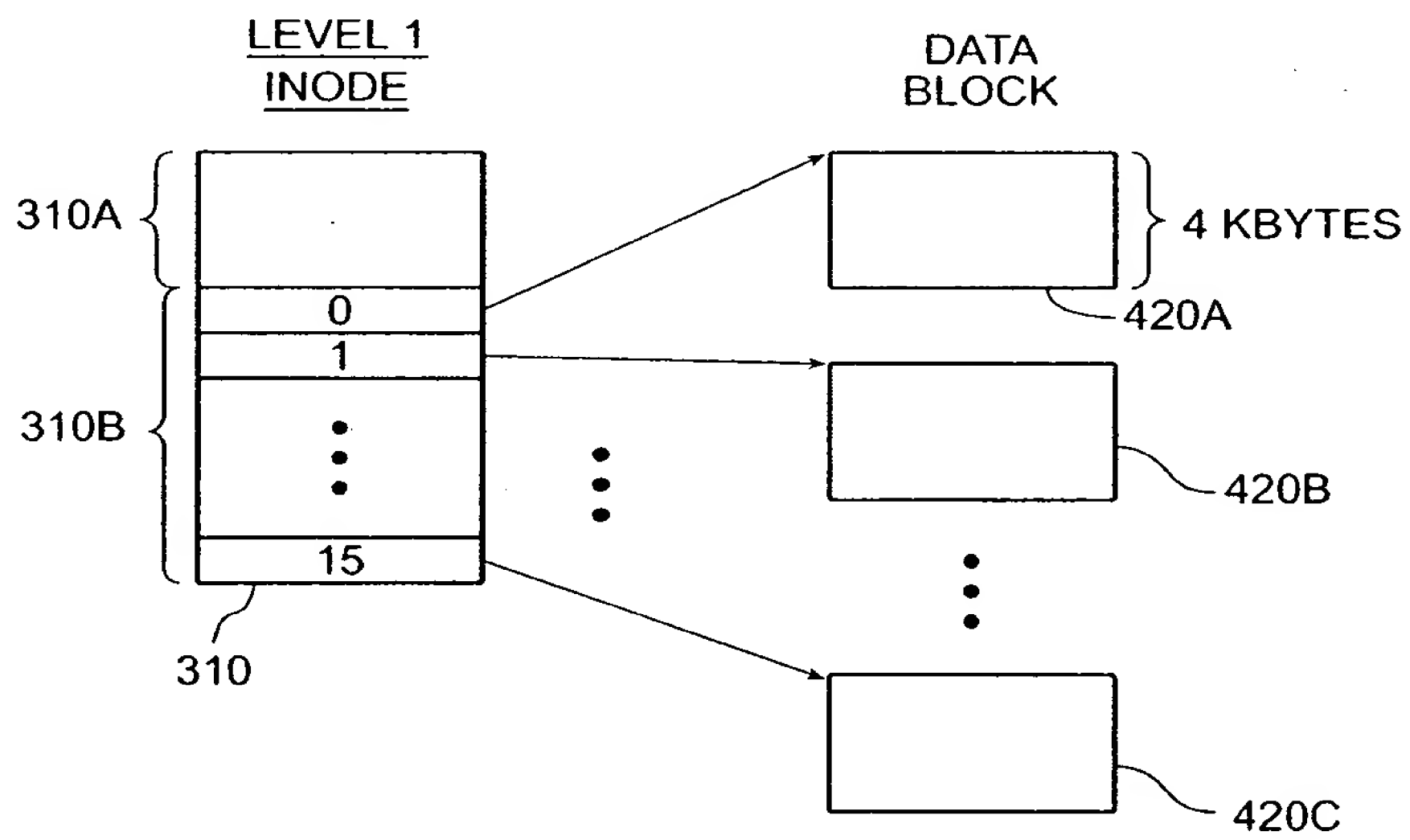


FIG. 4B

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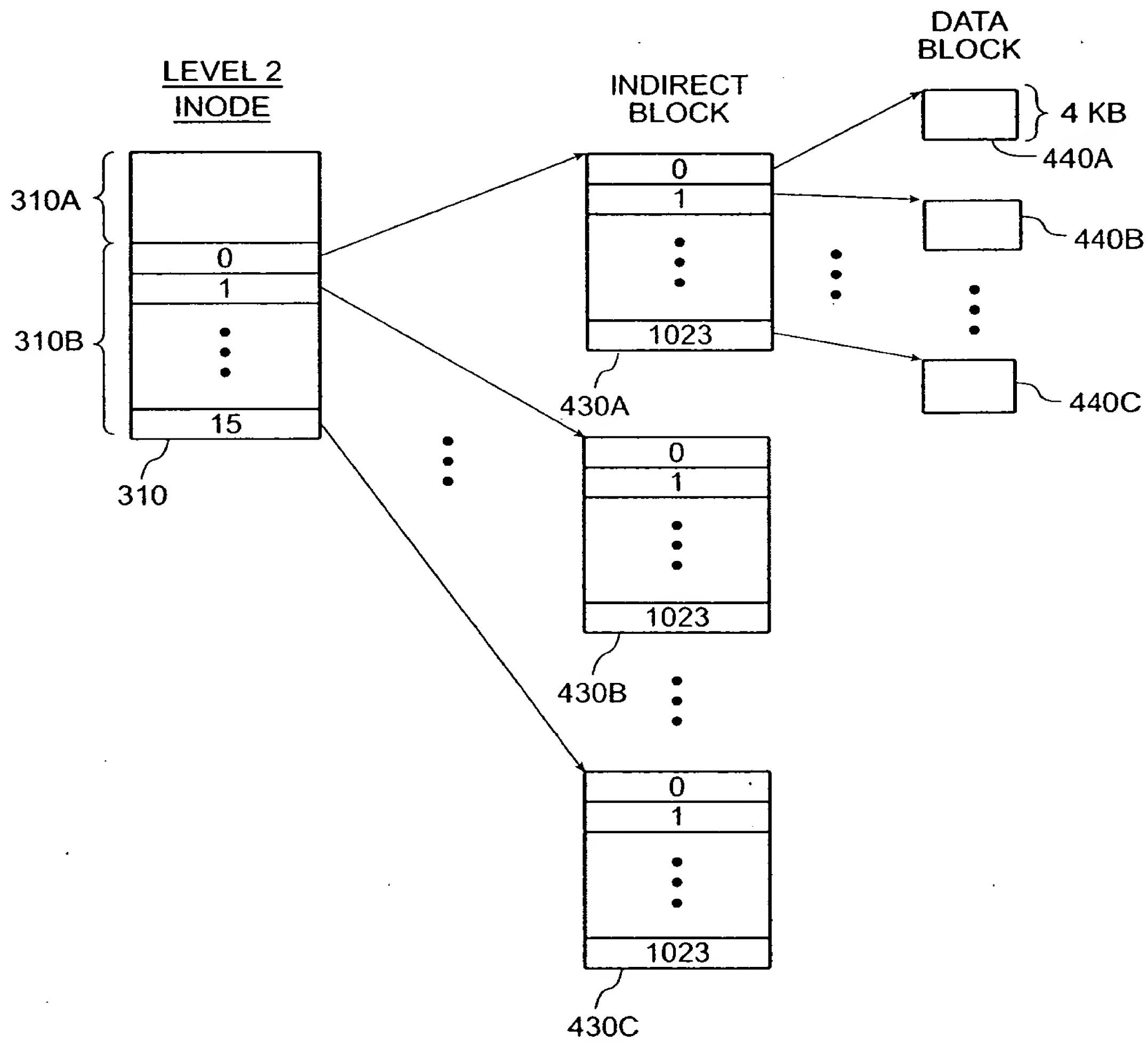


FIG. 4C

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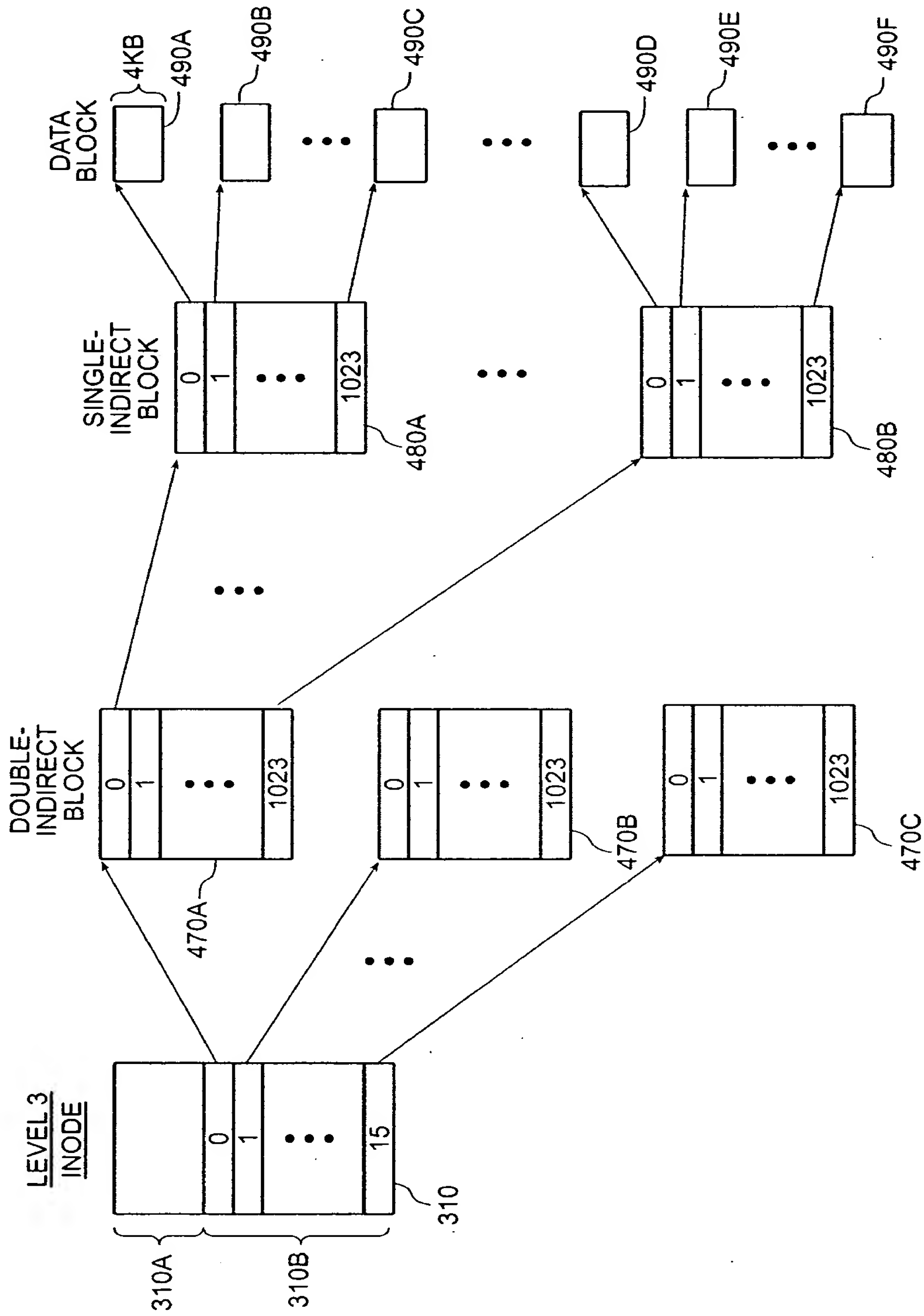


FIG. 4D

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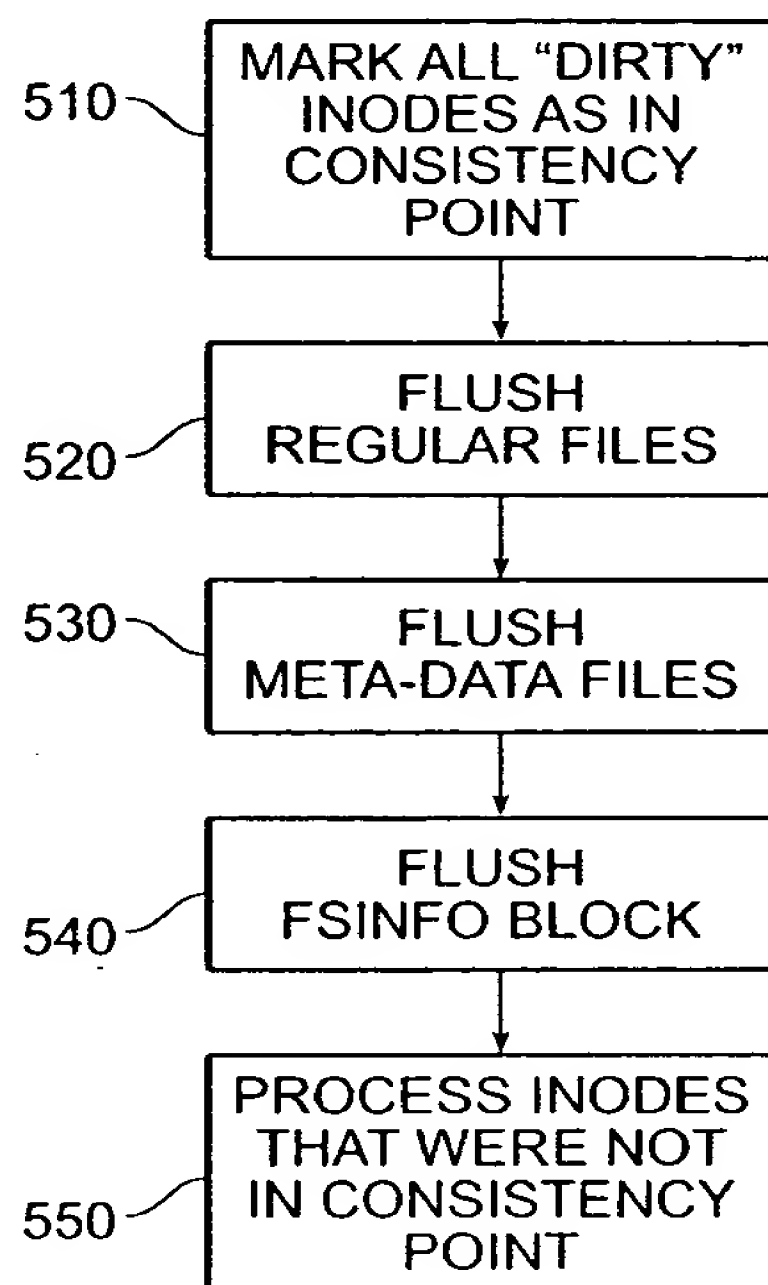


FIG. 5

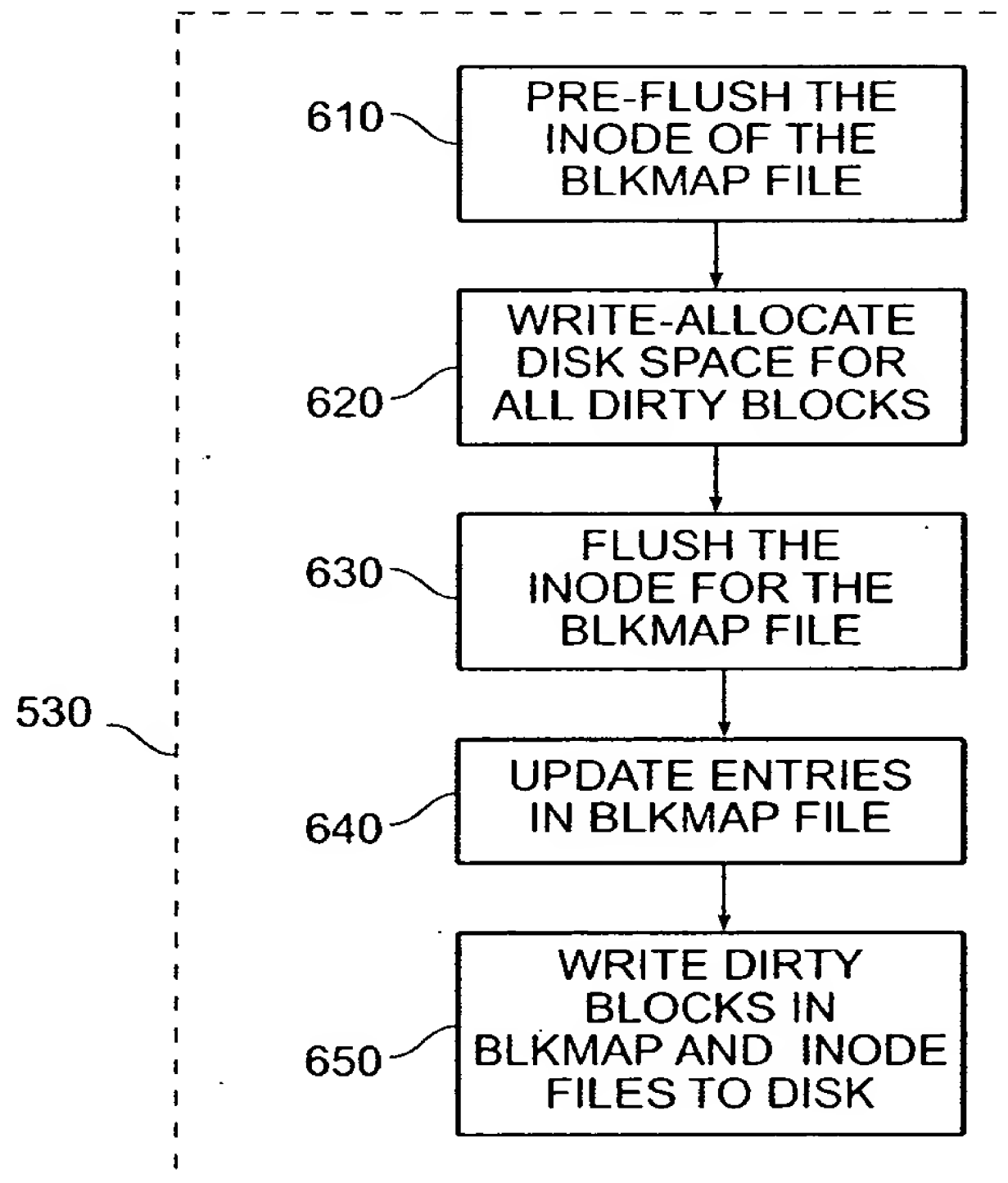


FIG. 6

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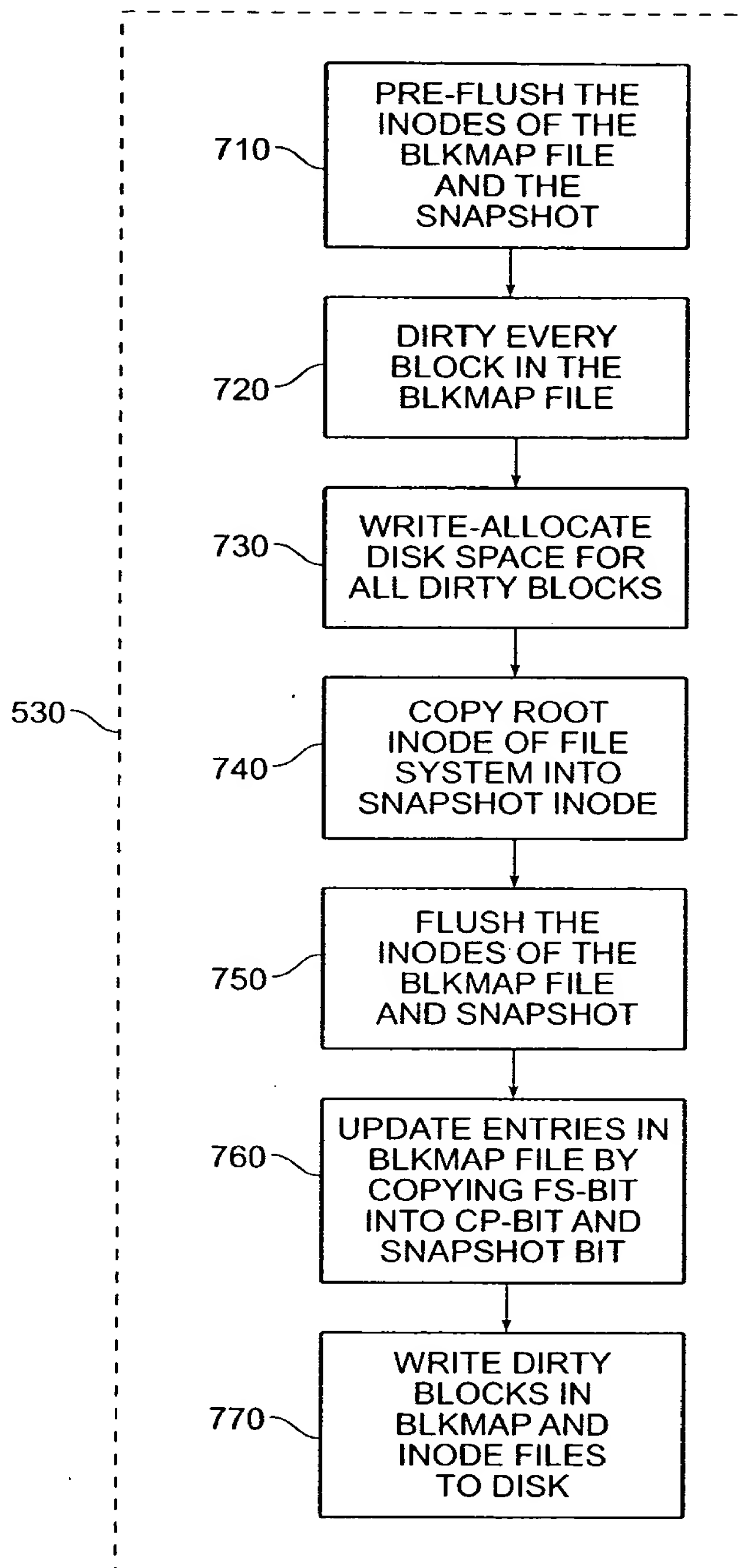


FIG. 7

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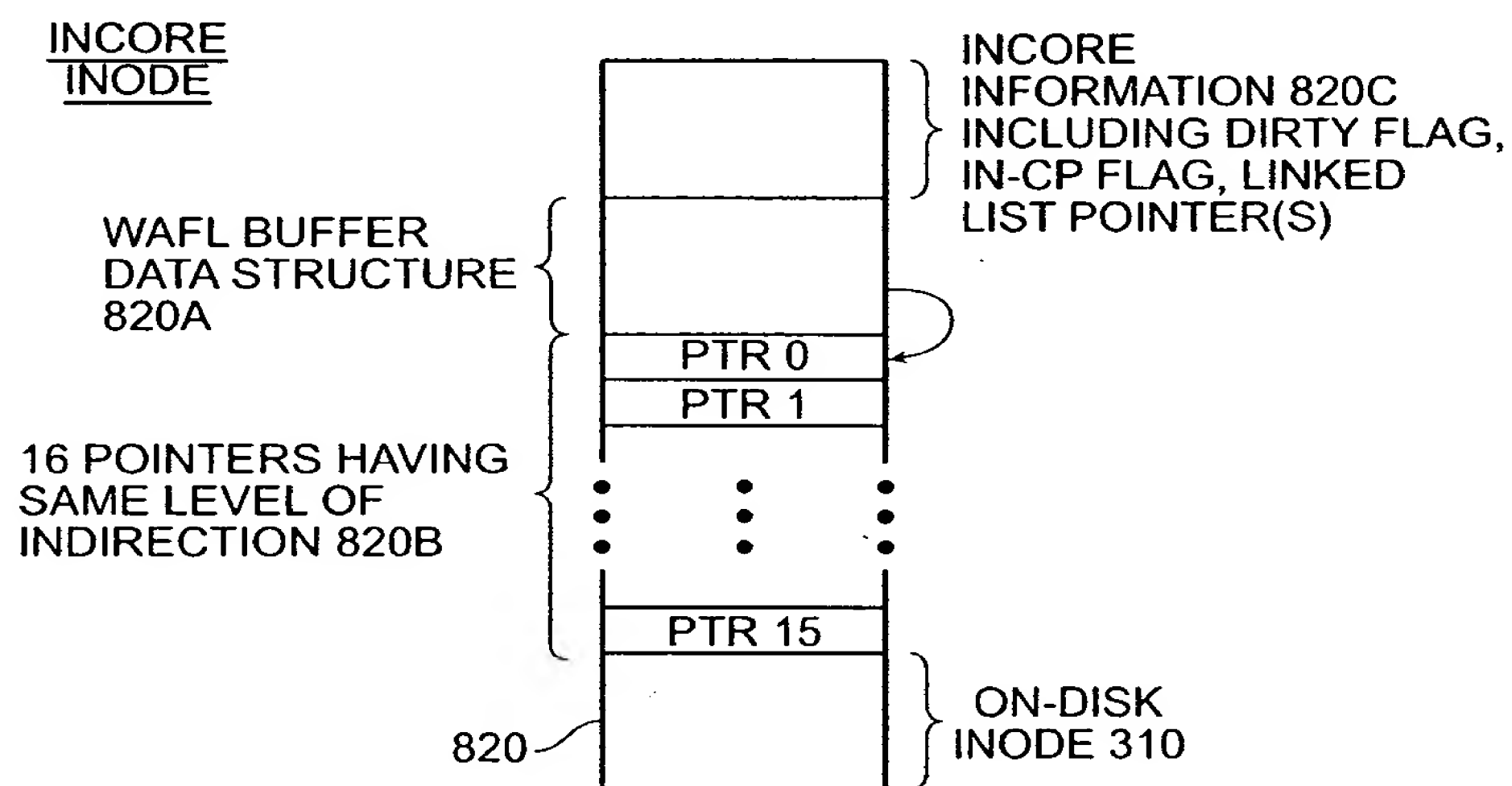


FIG.8

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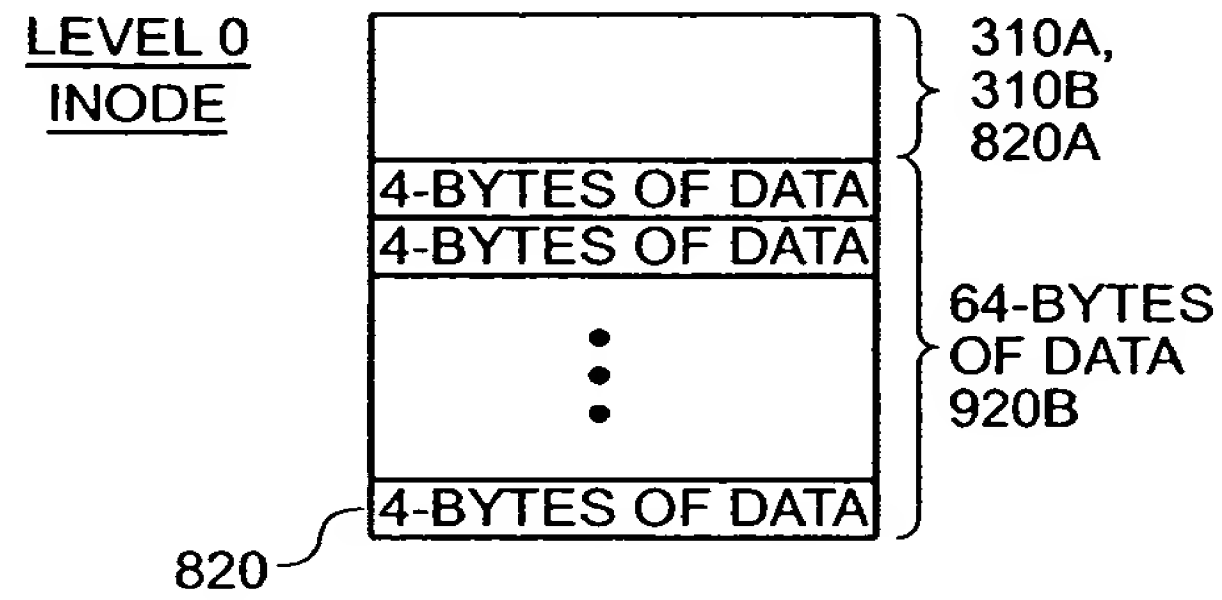


FIG. 9A

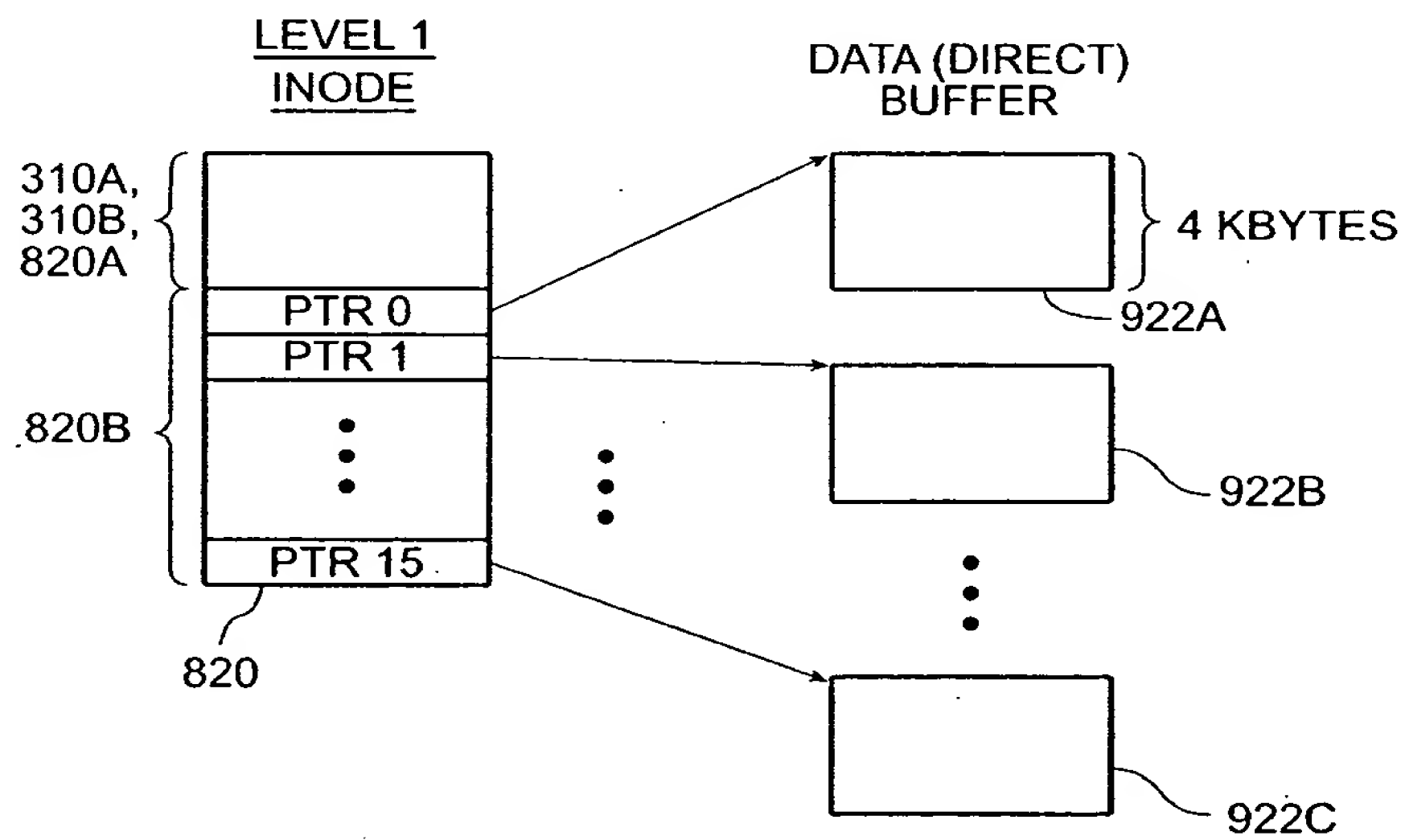


FIG. 9B

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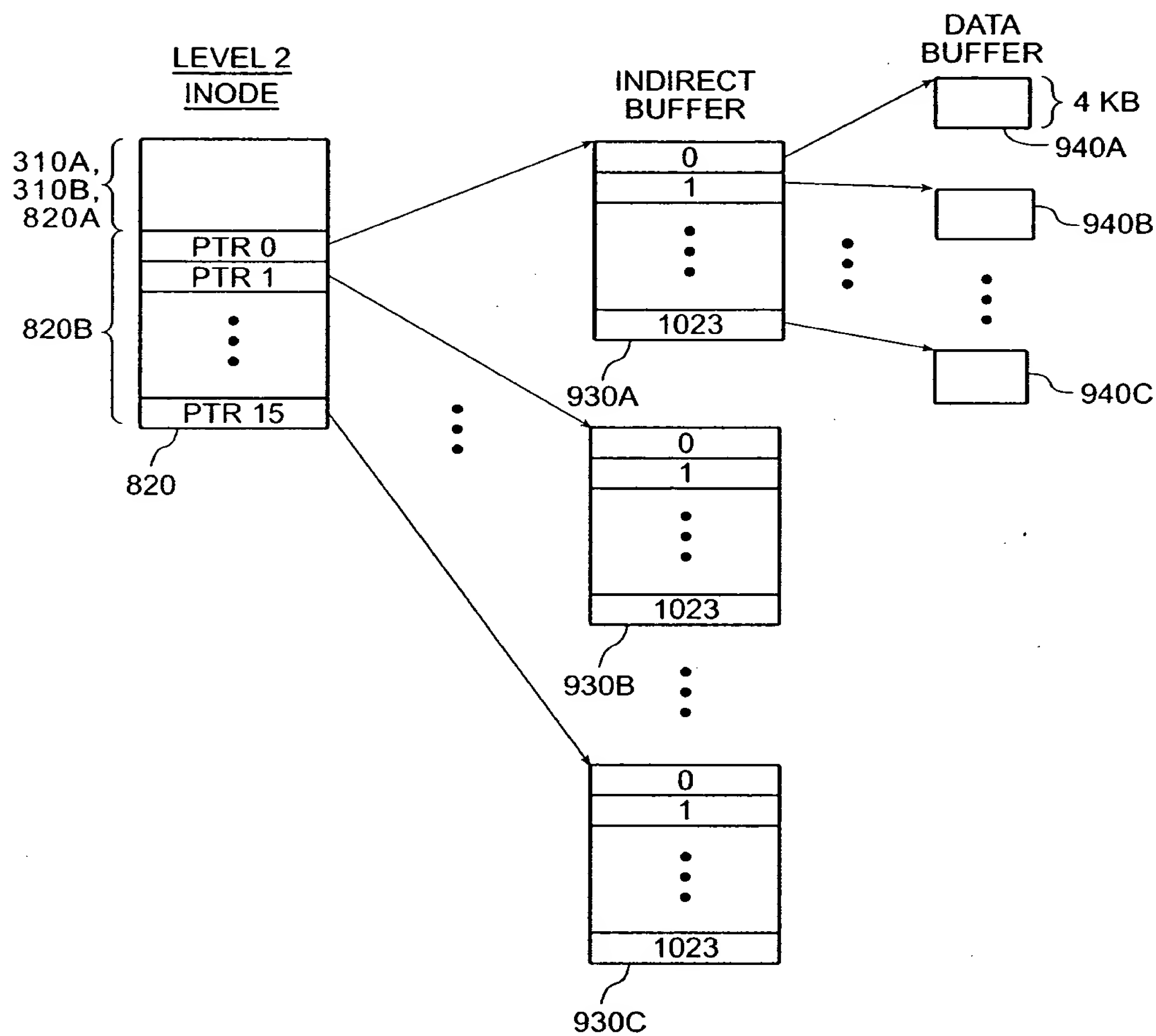


FIG. 9C

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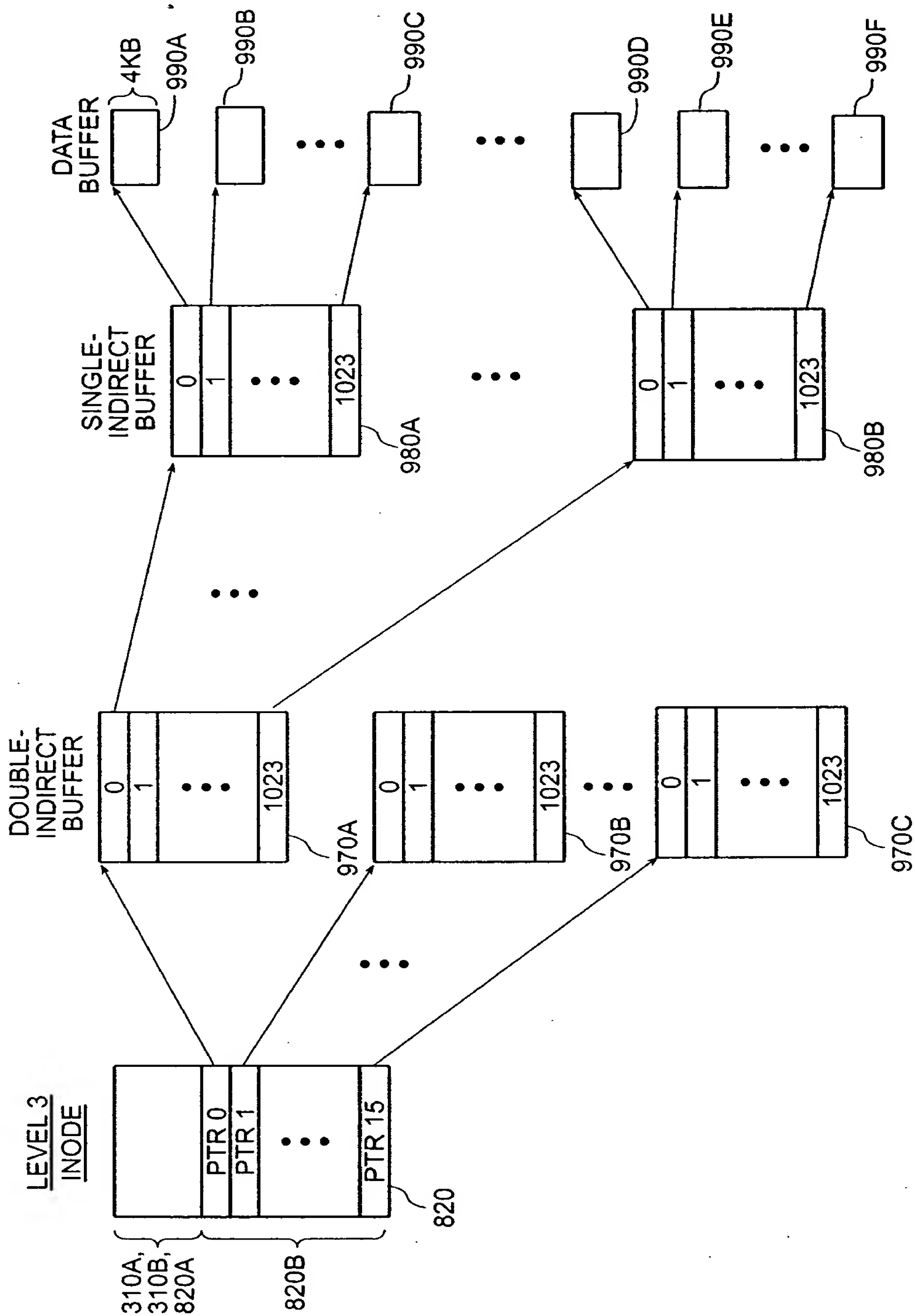


FIG. 9D

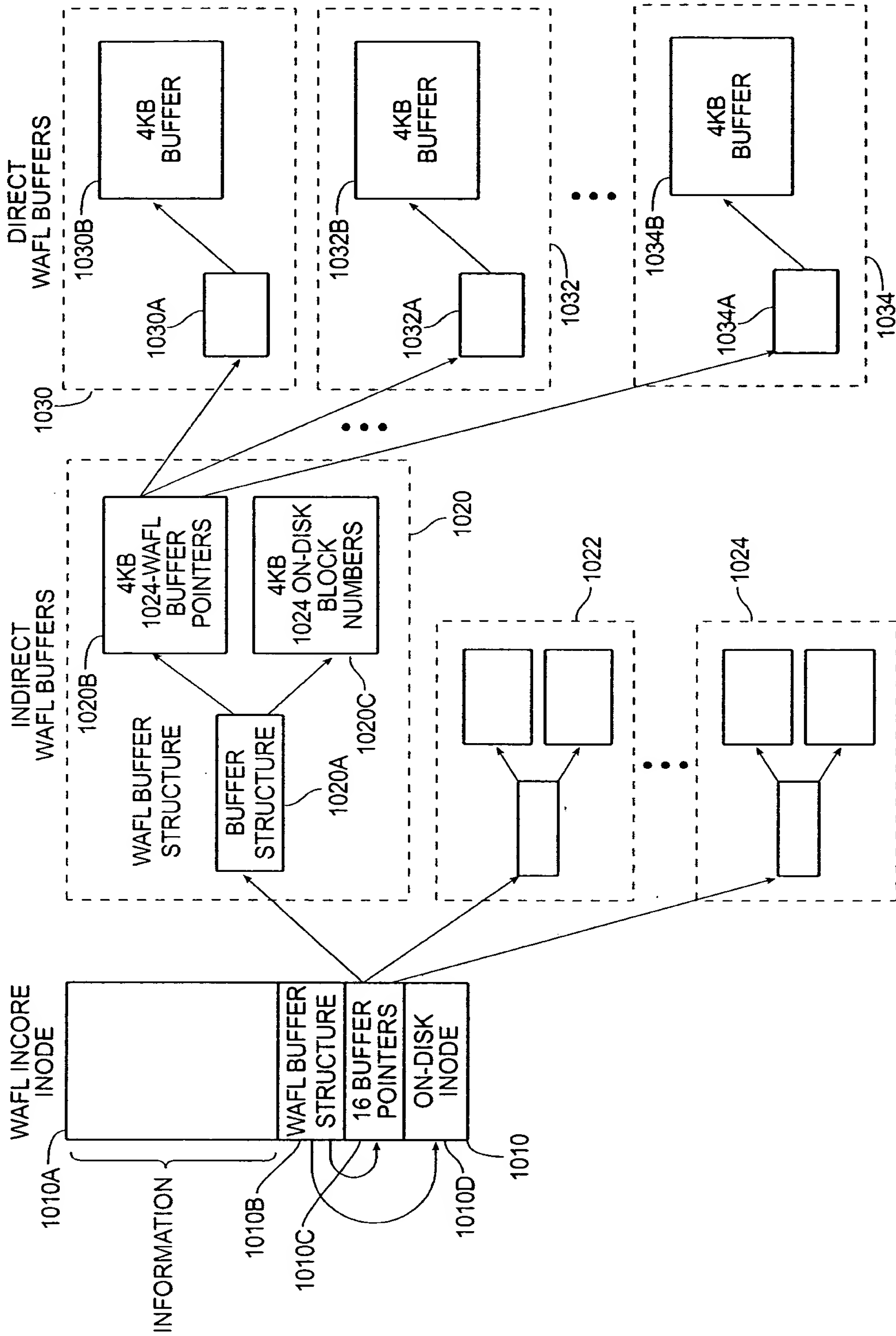


FIG. 10

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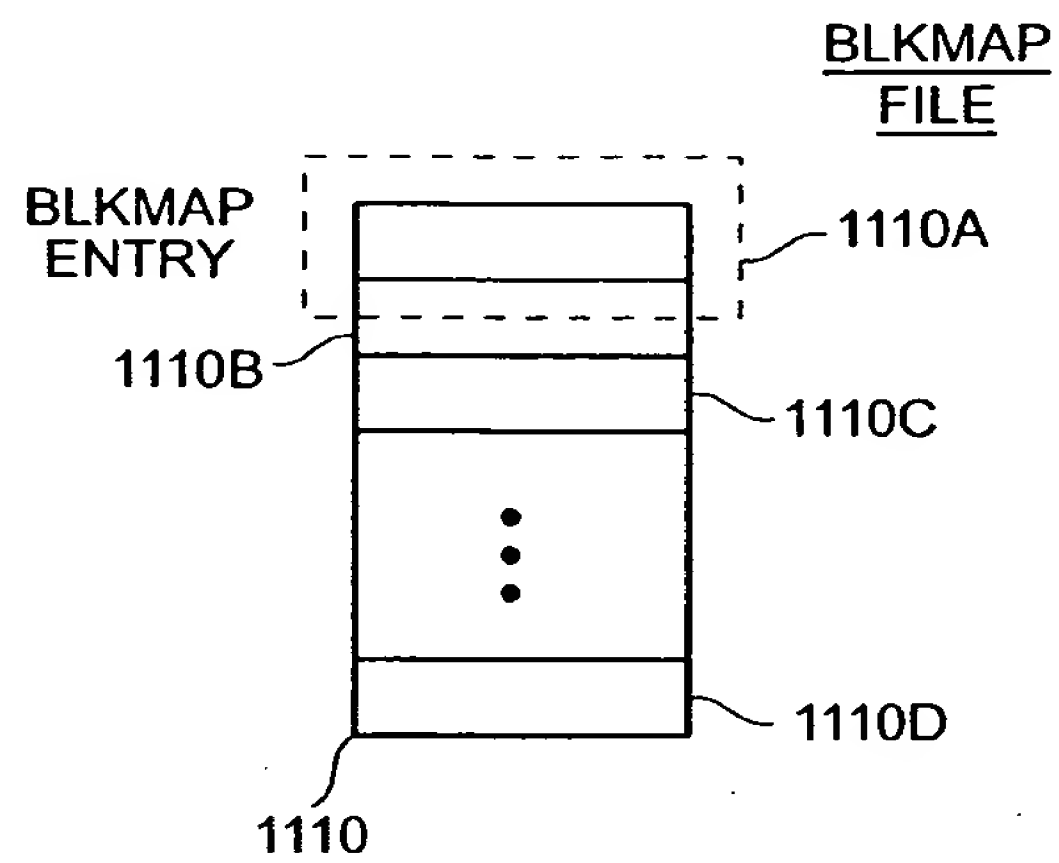


FIG. 11A

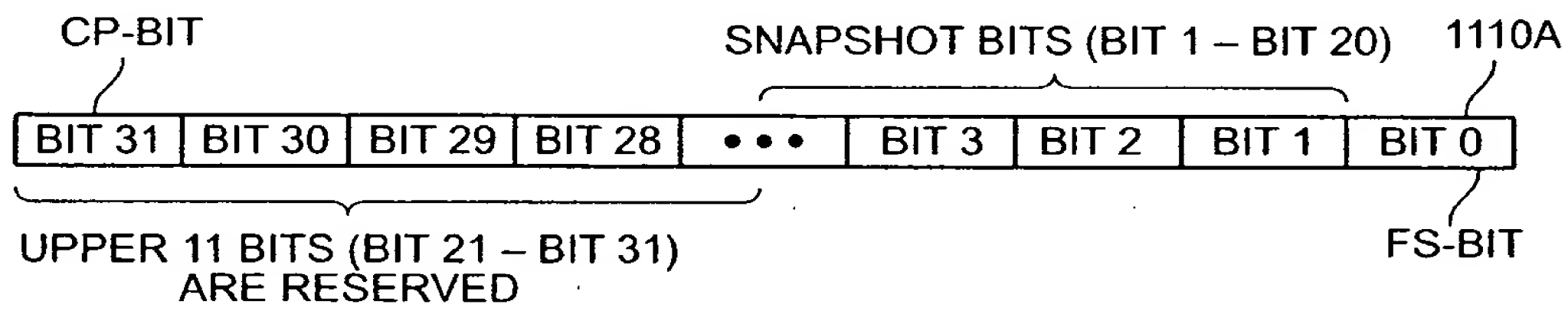


FIG. 11B

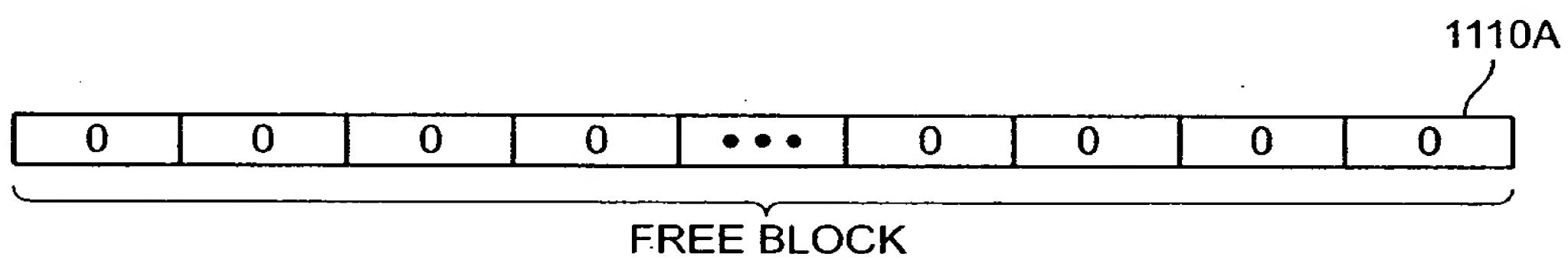


FIG. 11C

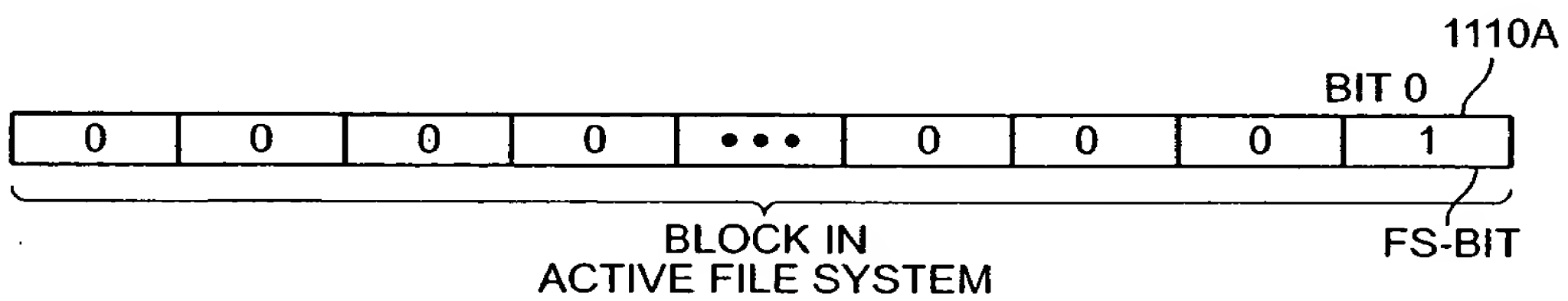


FIG. 11D



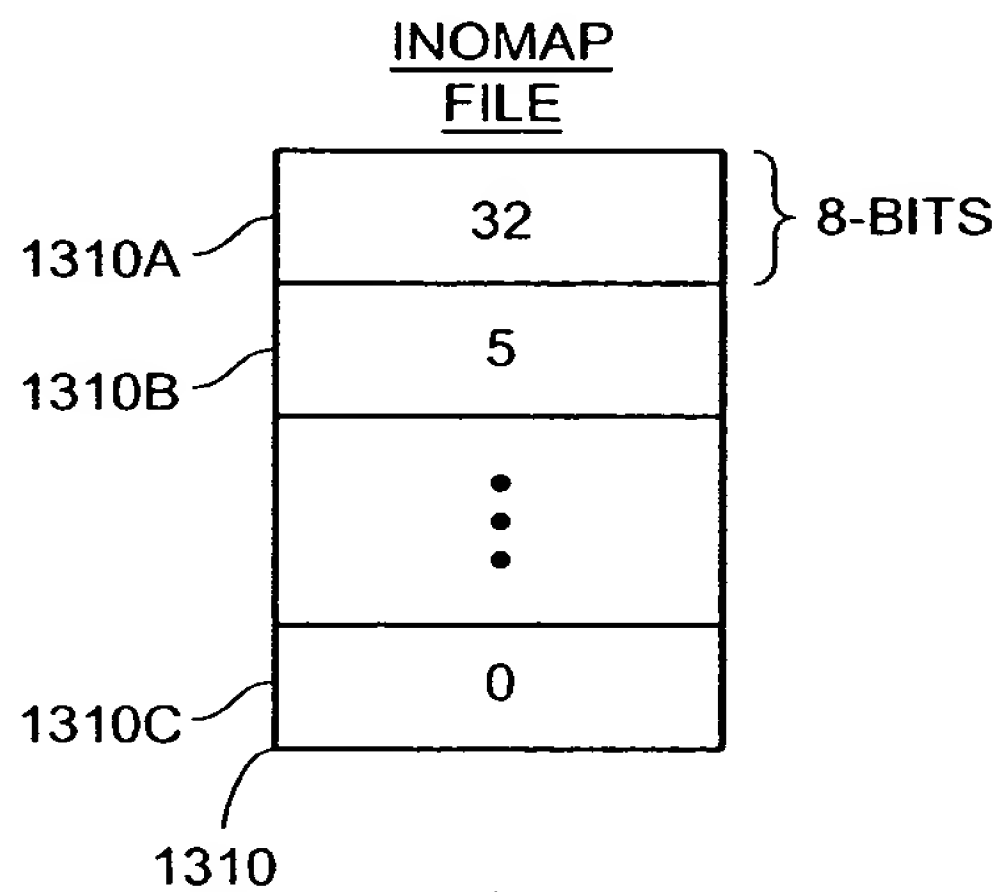


FIG. 13A

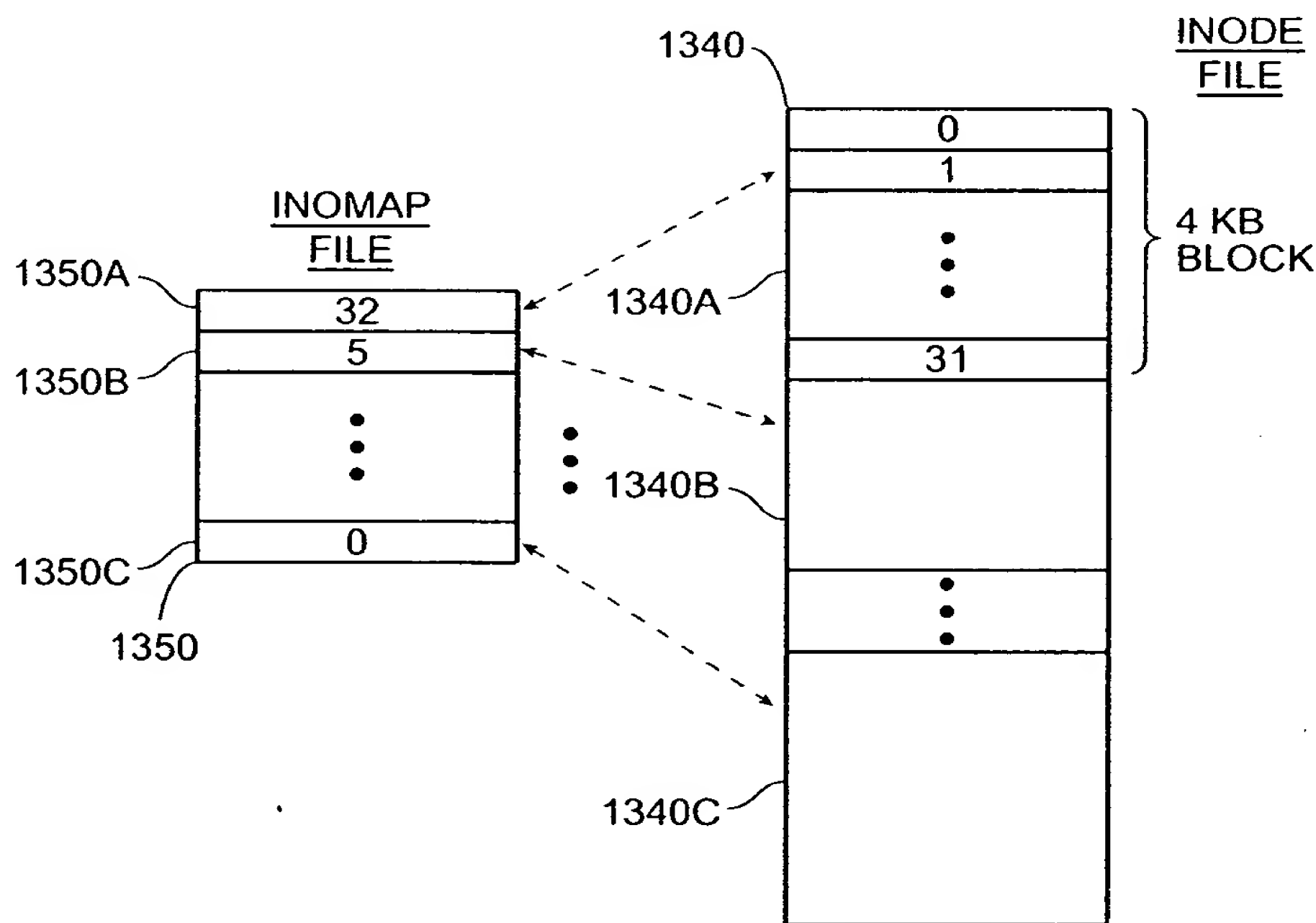


FIG. 13B

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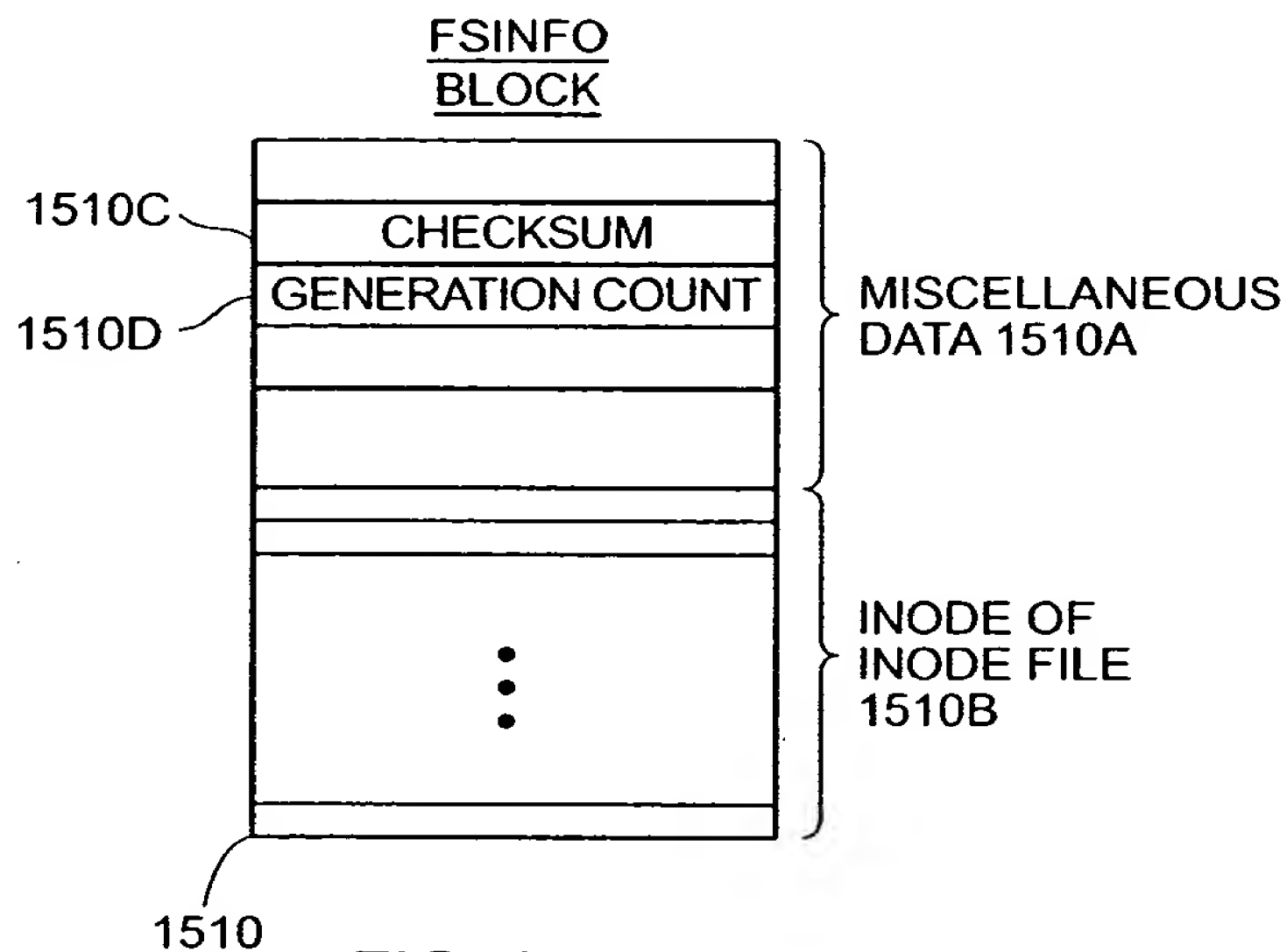


FIG. 15

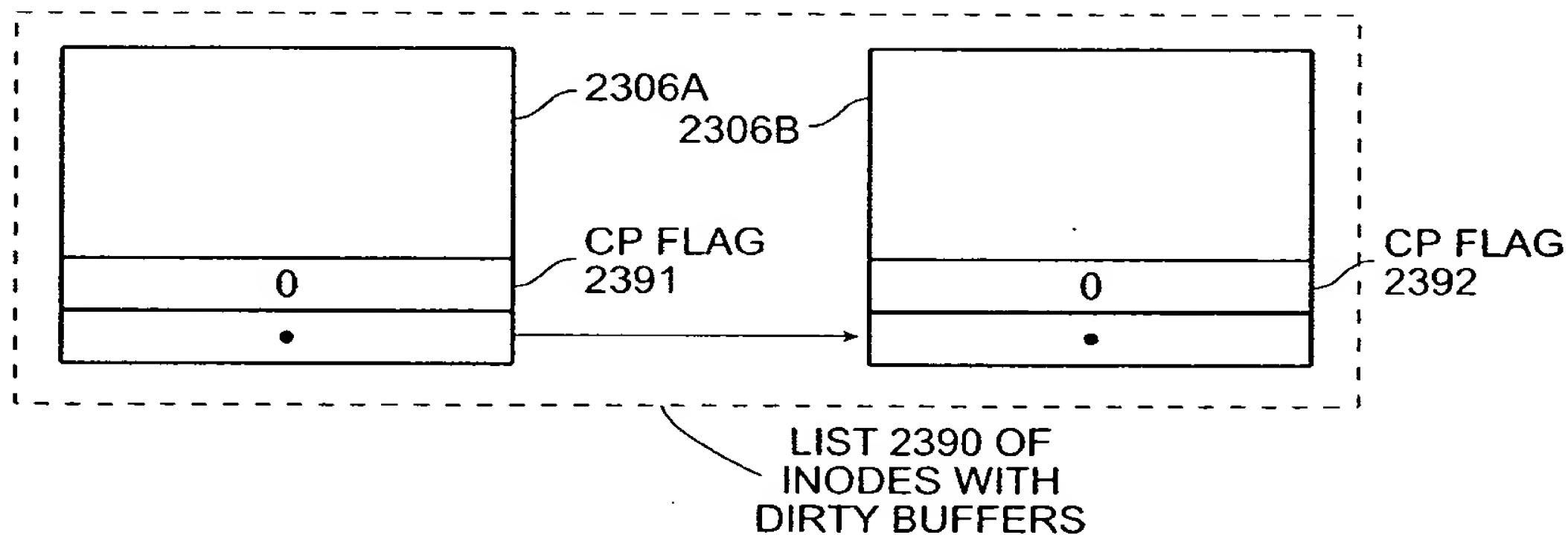


FIG. 17A

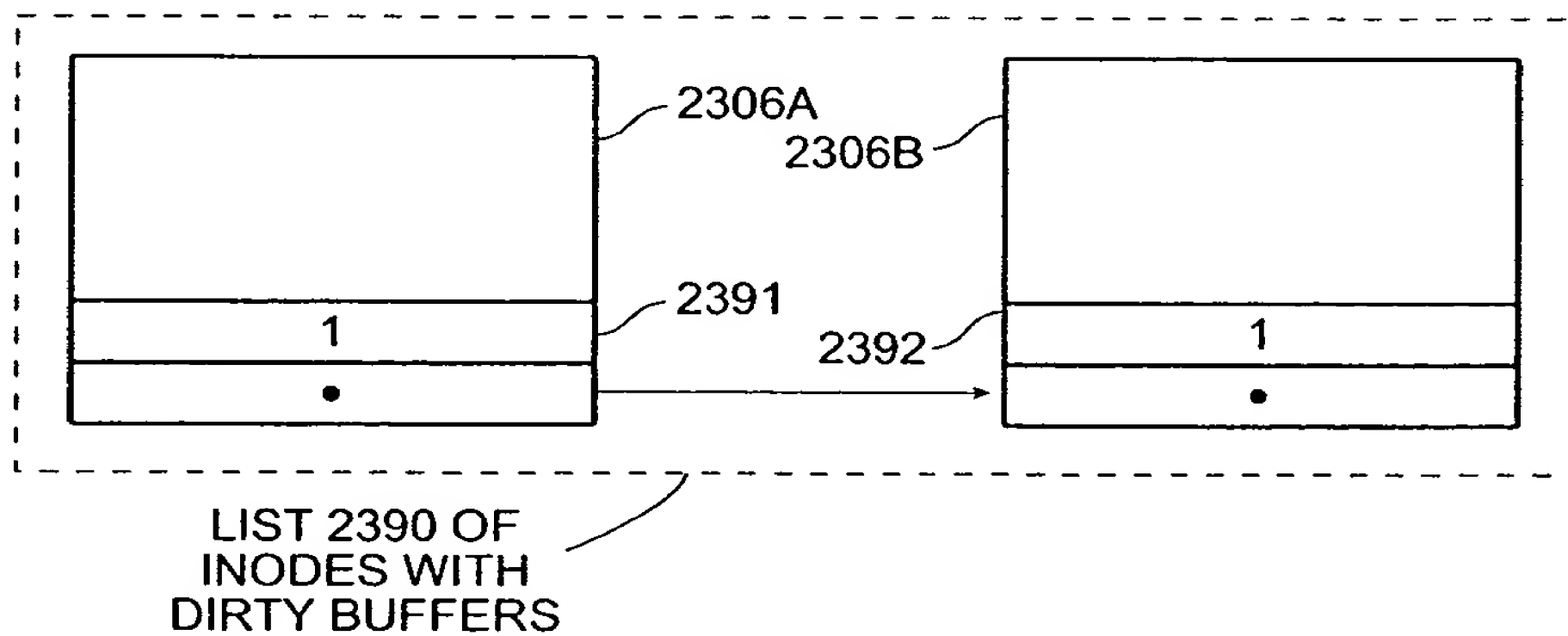


FIG. 17I

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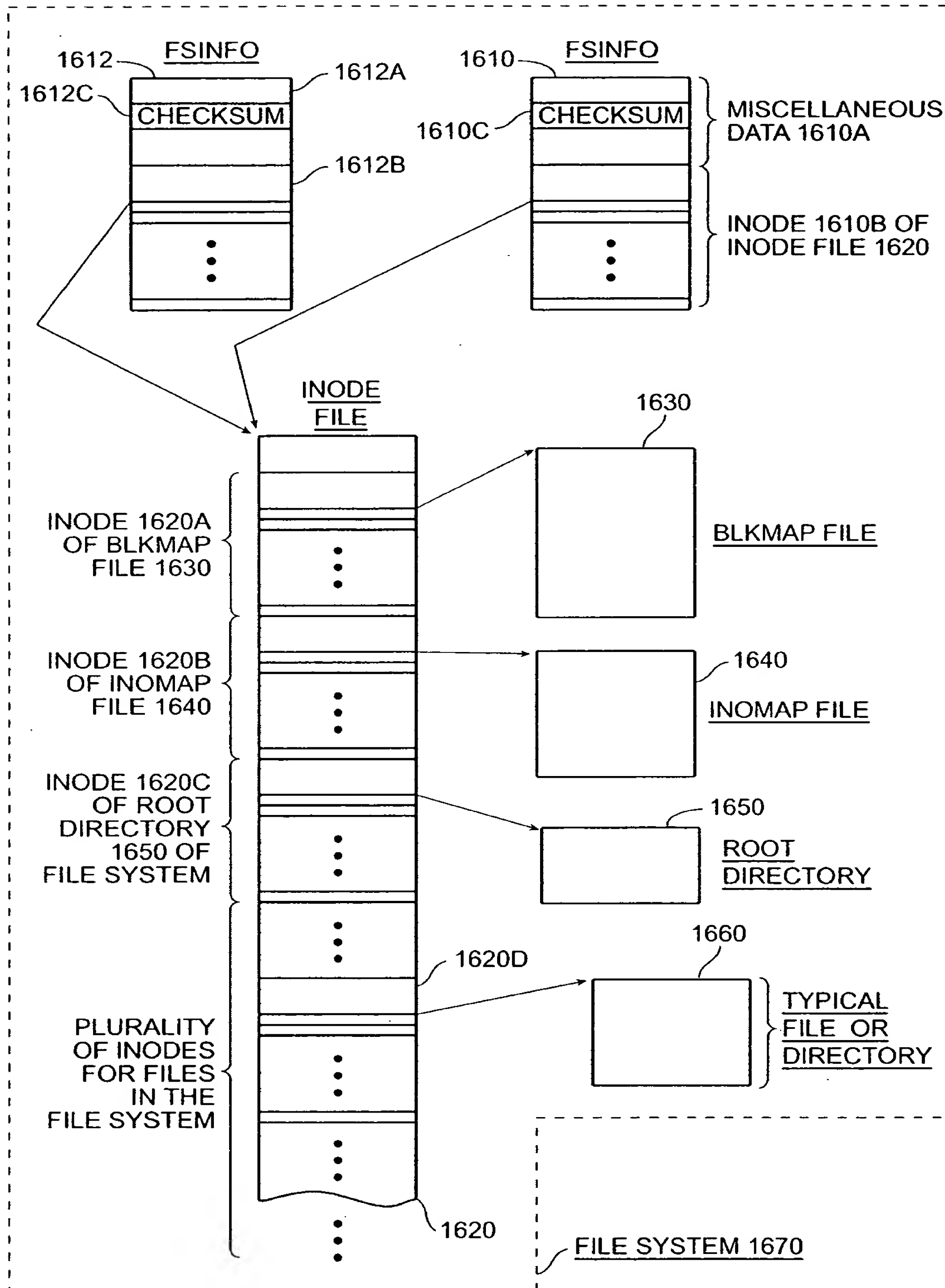


FIG. 16

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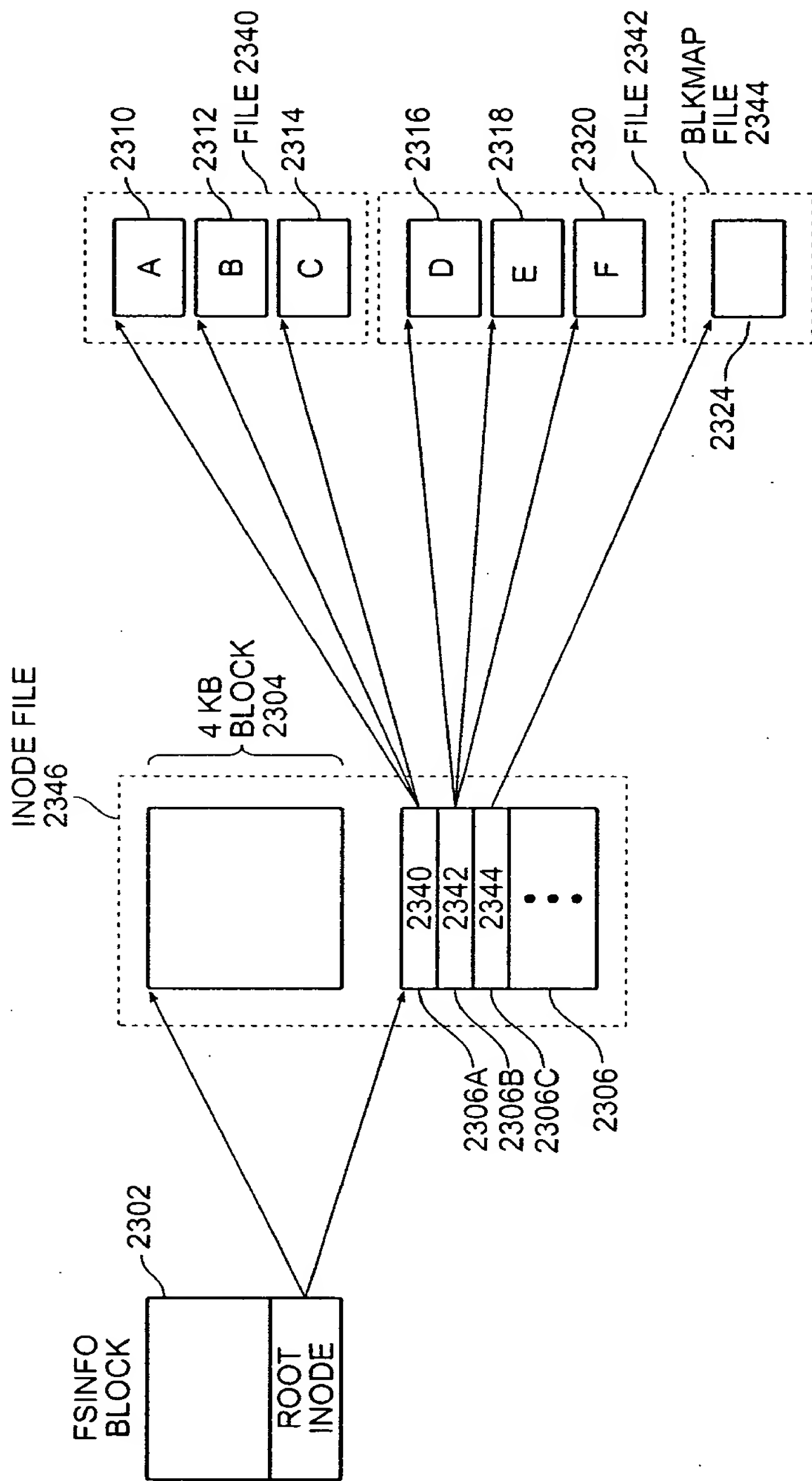


FIG. 17B

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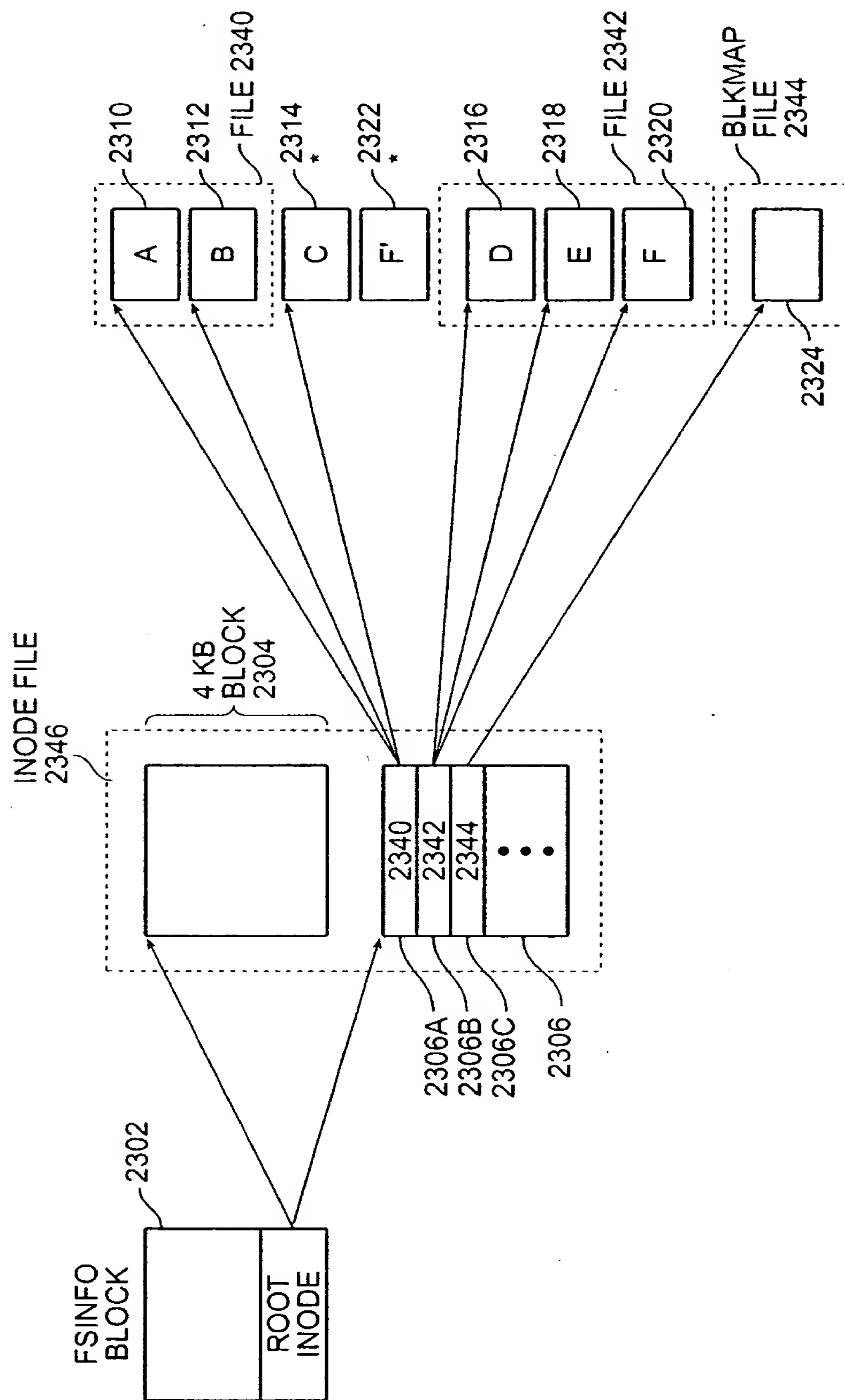


FIG. 17C

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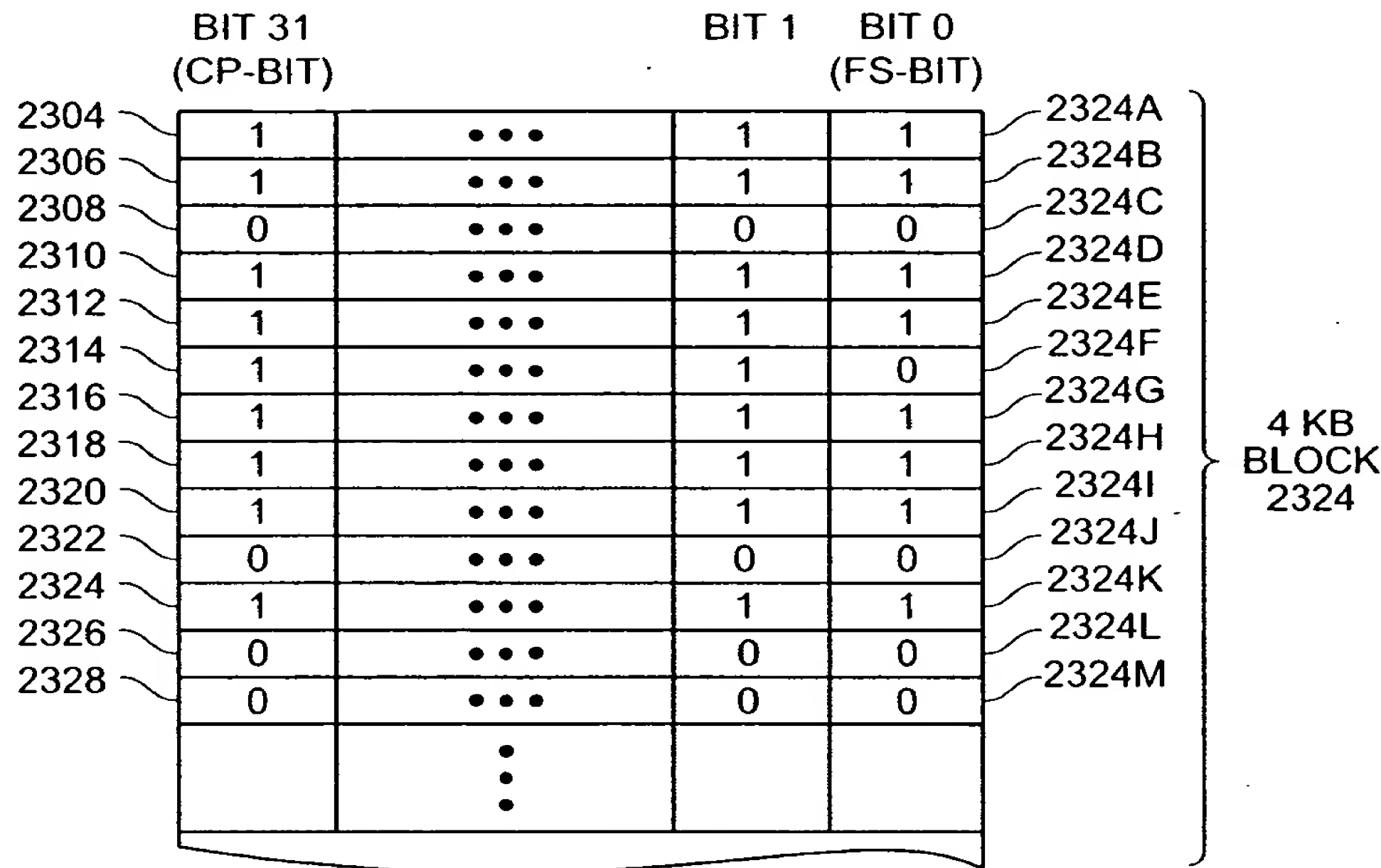


FIG. 17D

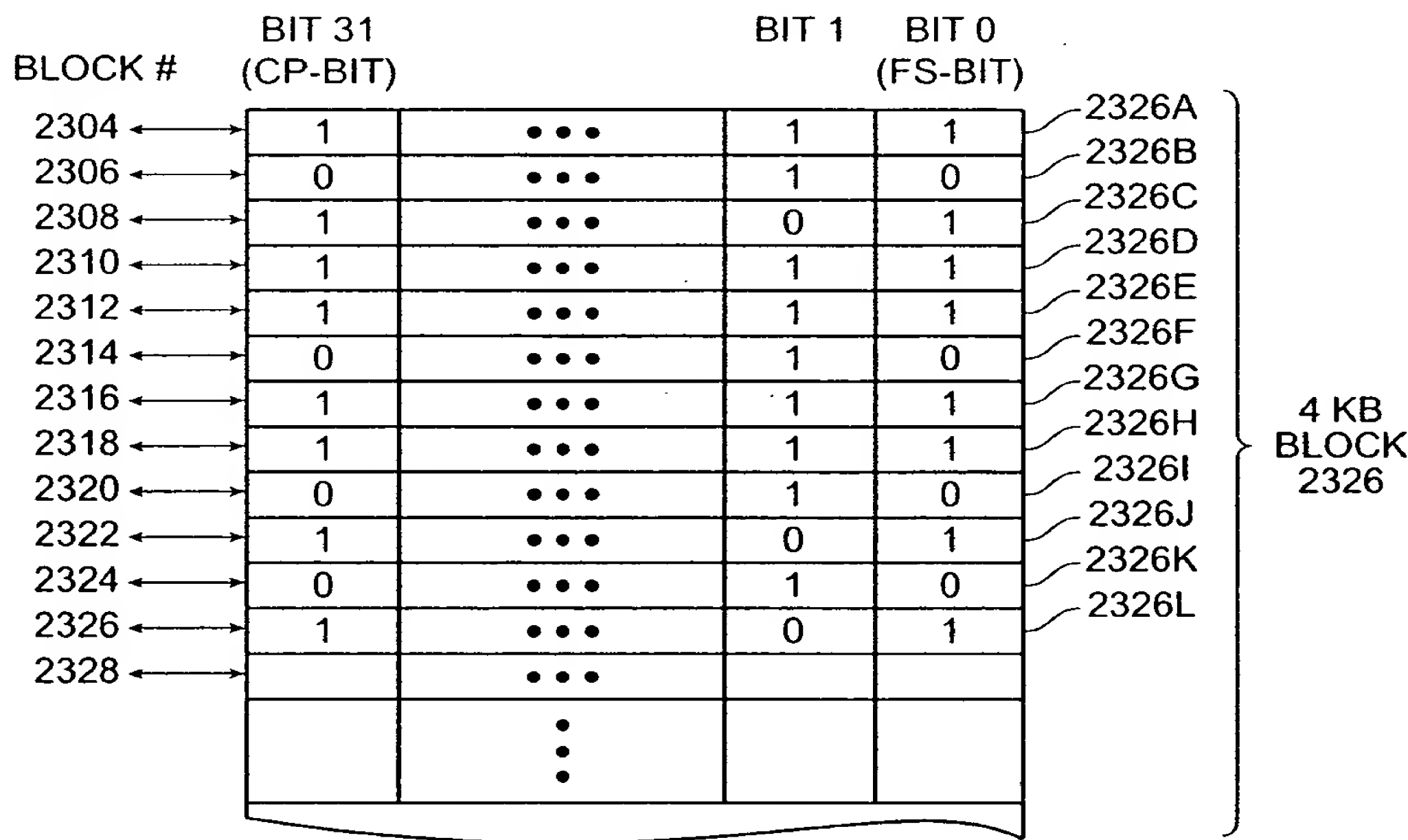


FIG. 17J

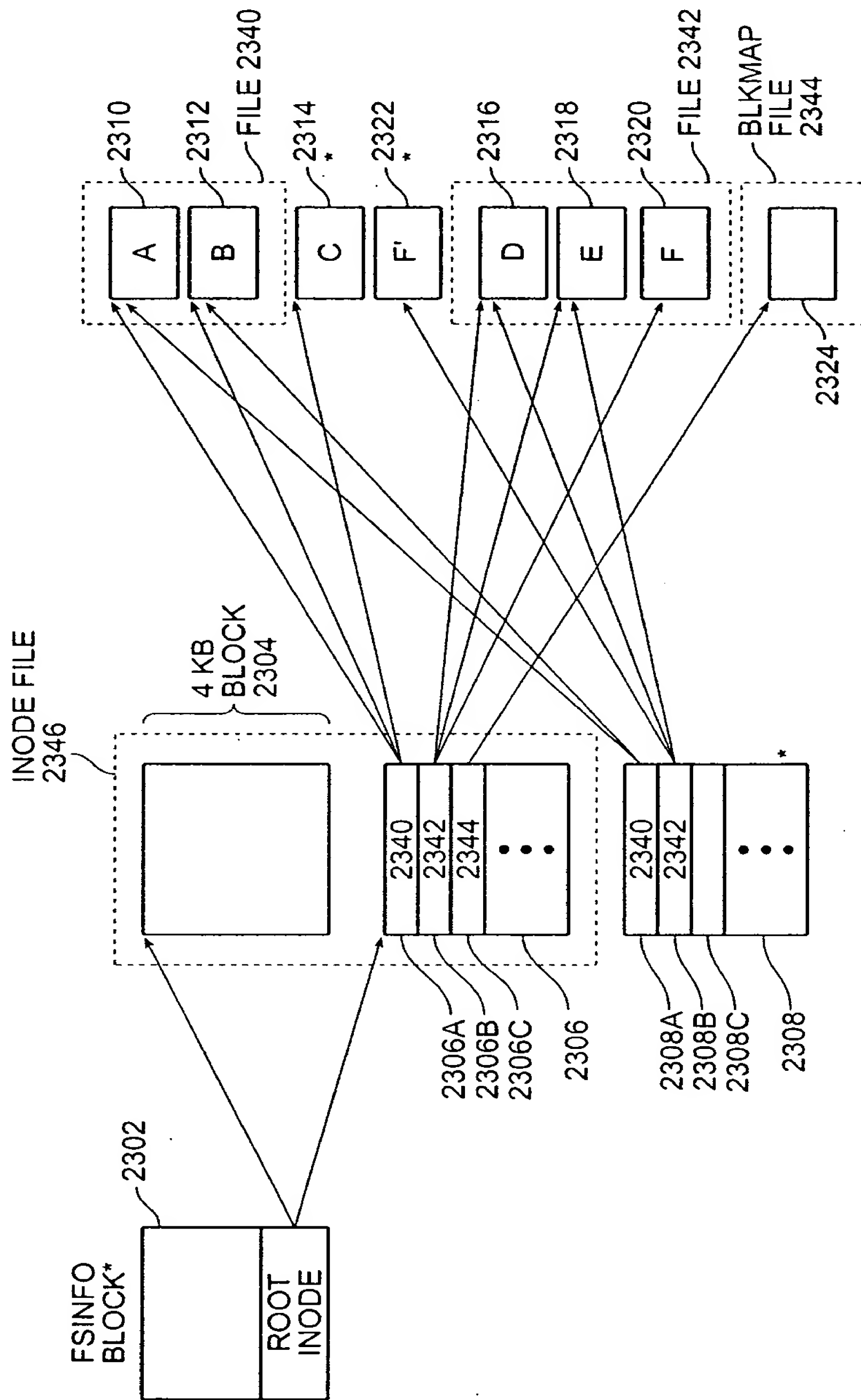


FIG. 17E

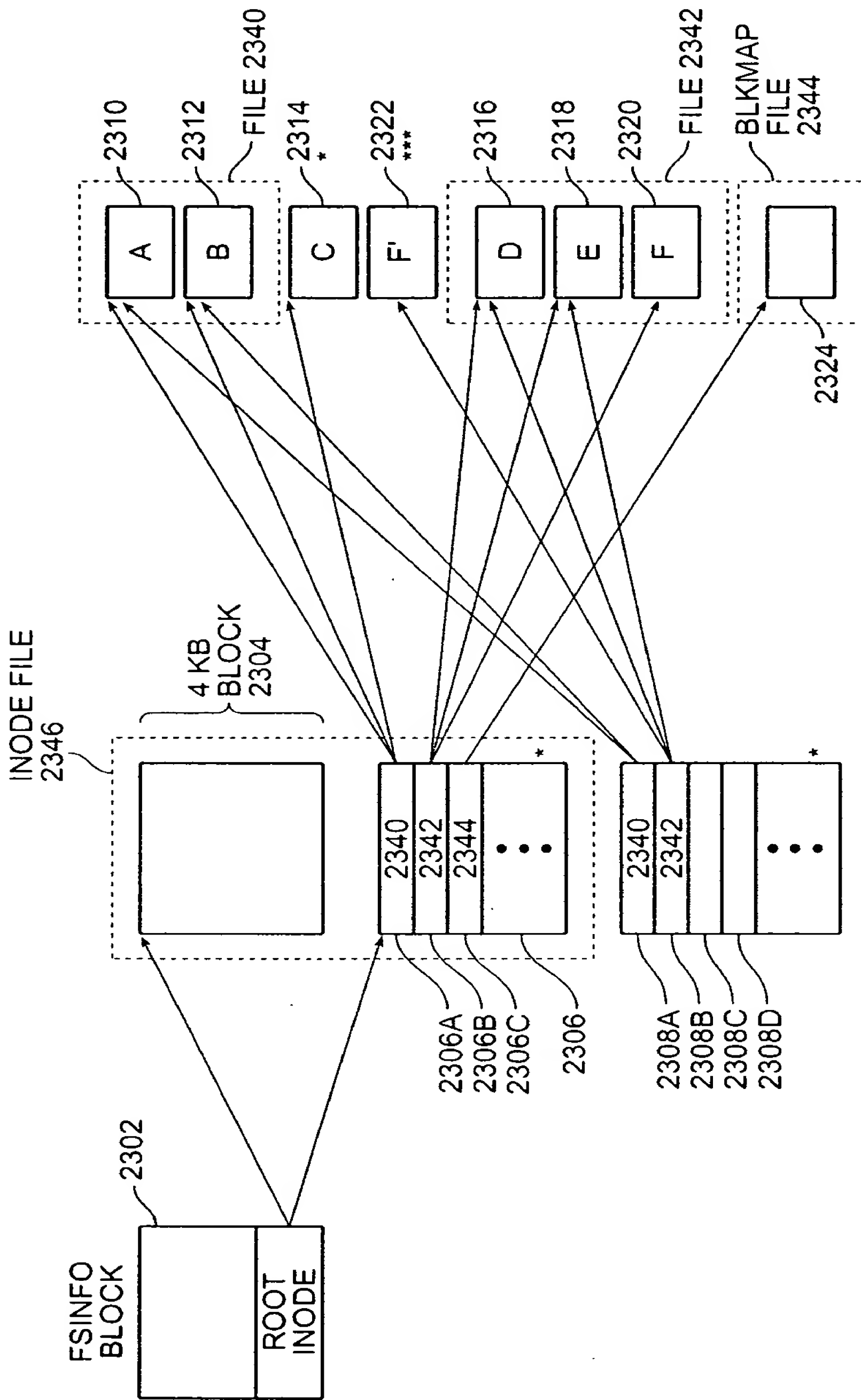


FIG. 17F

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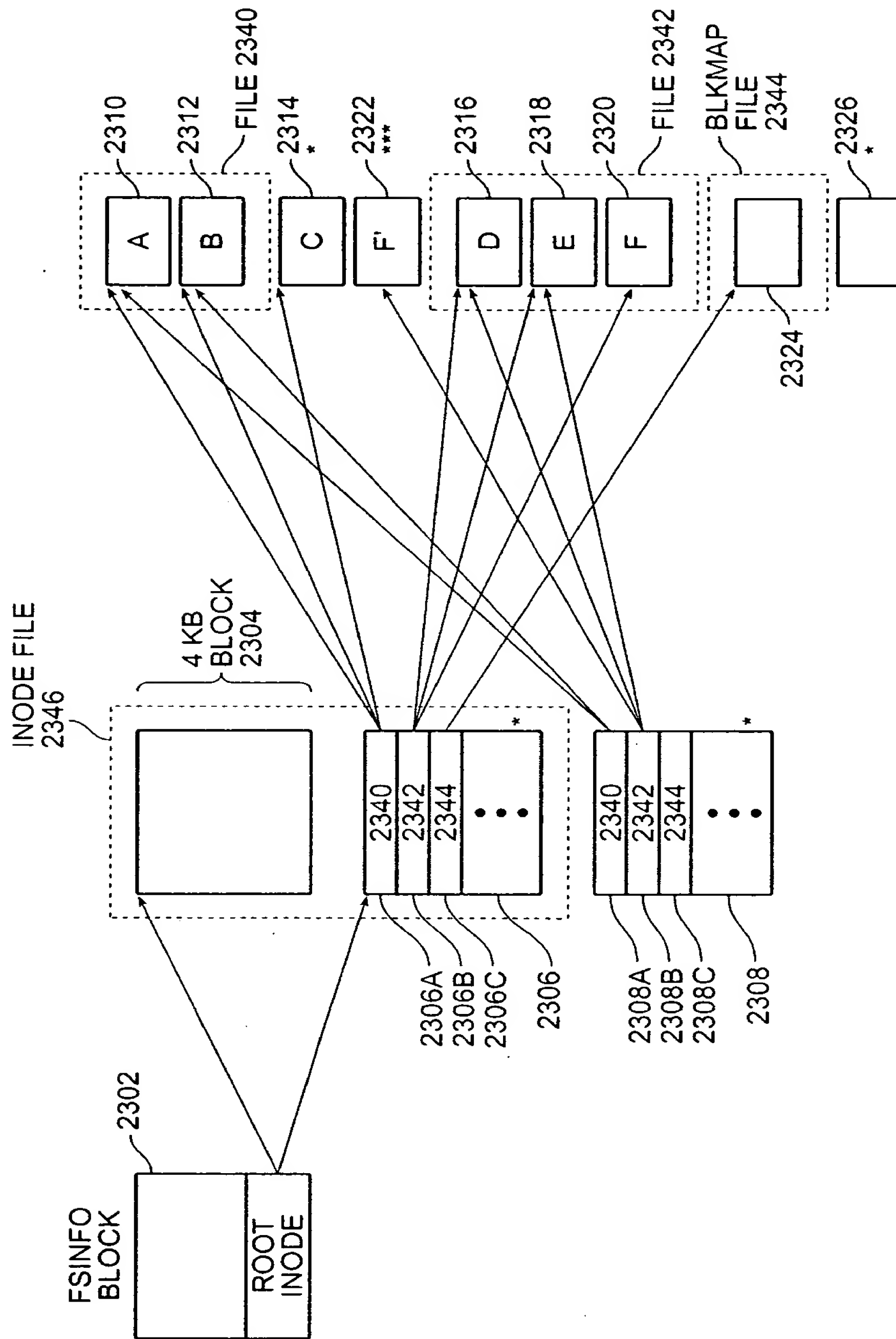


FIG. 17G

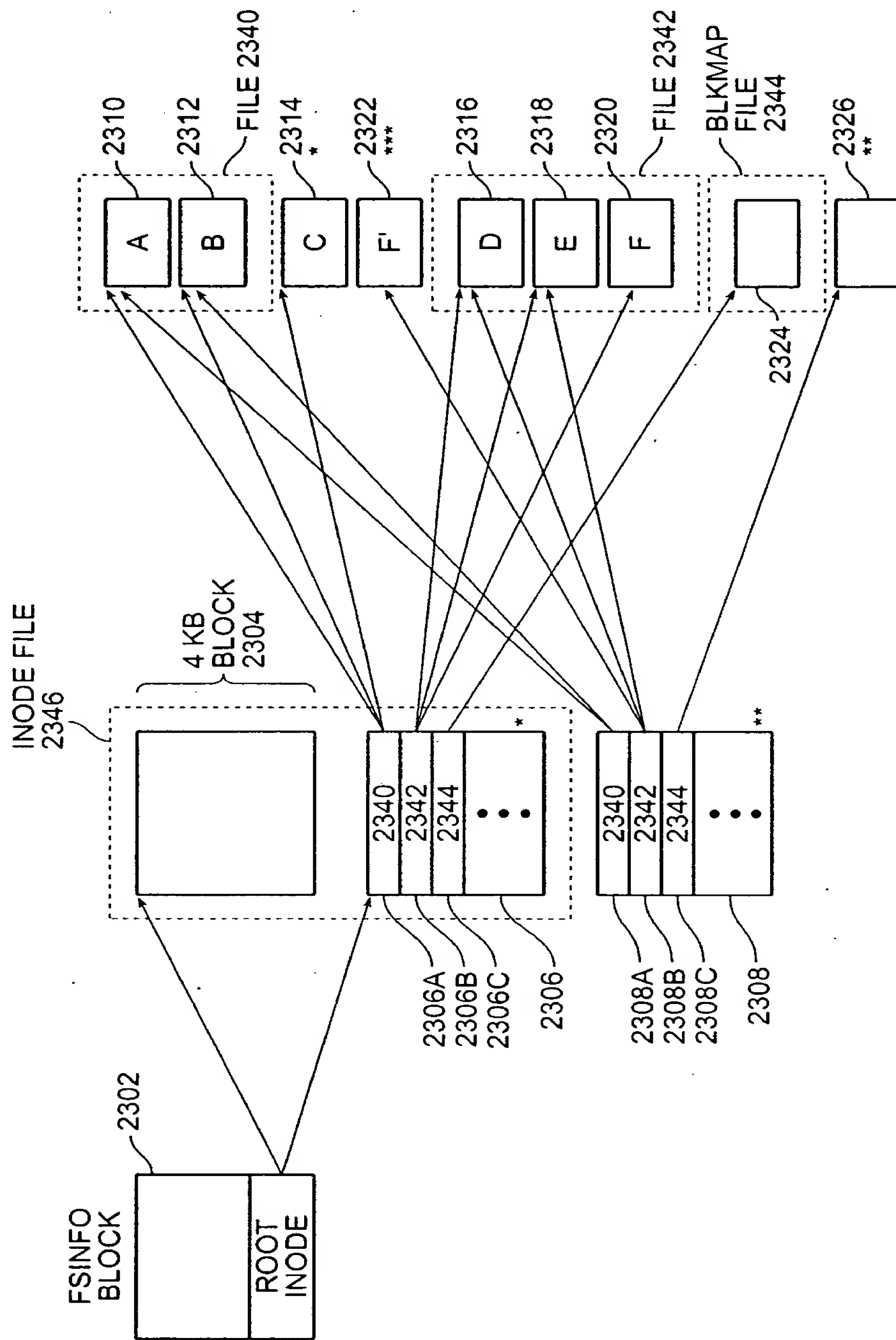


FIG. 17H

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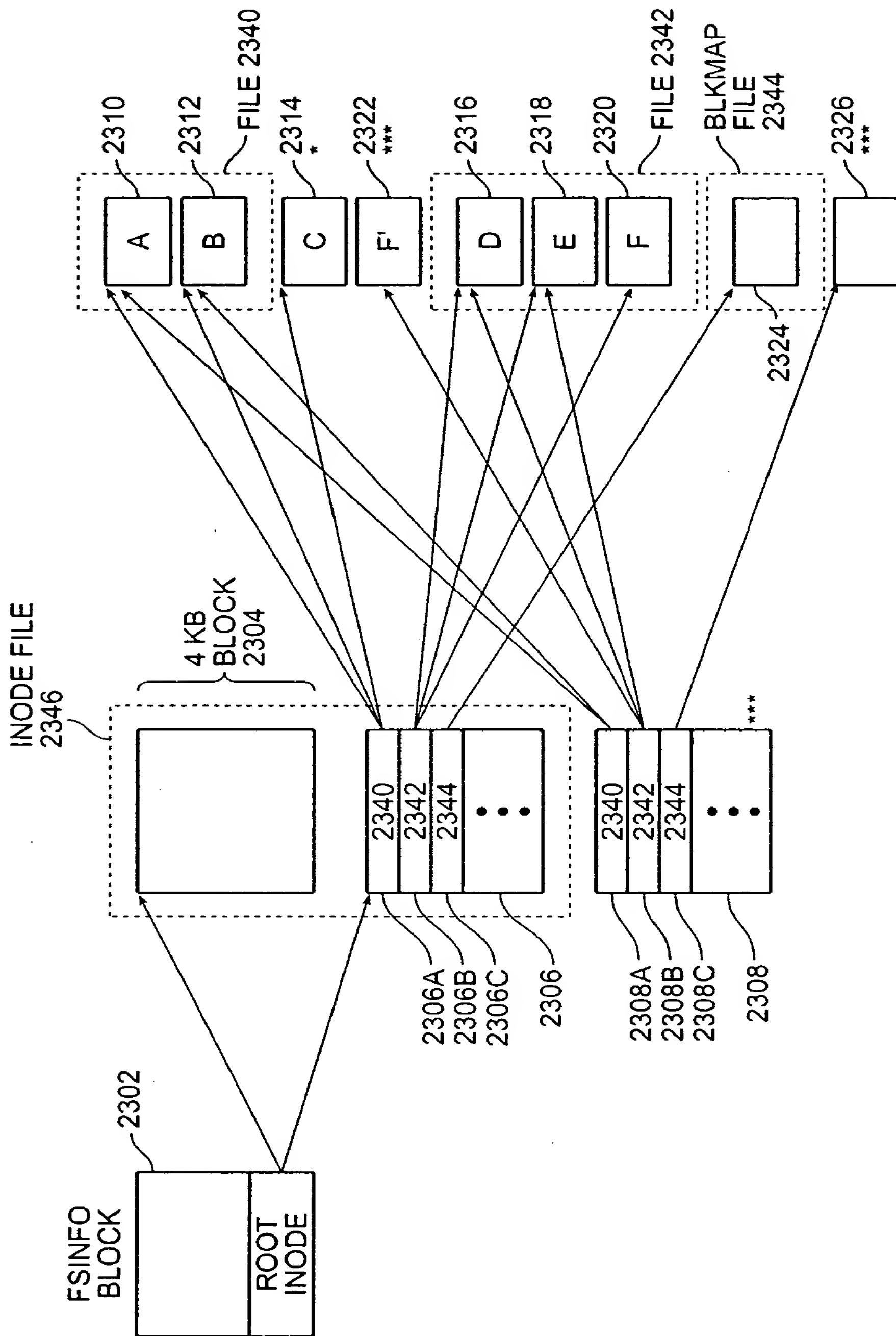
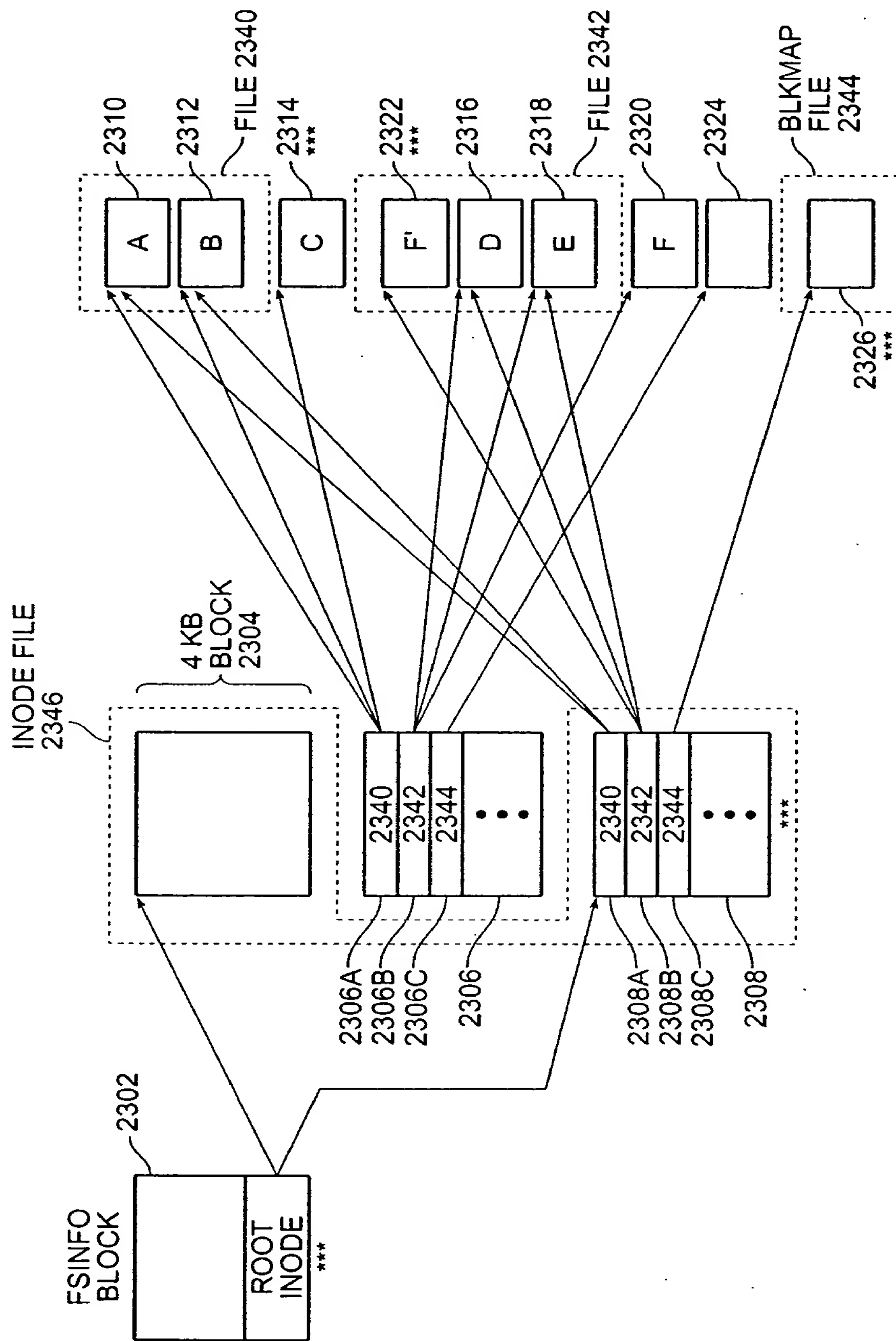


FIG. 17K

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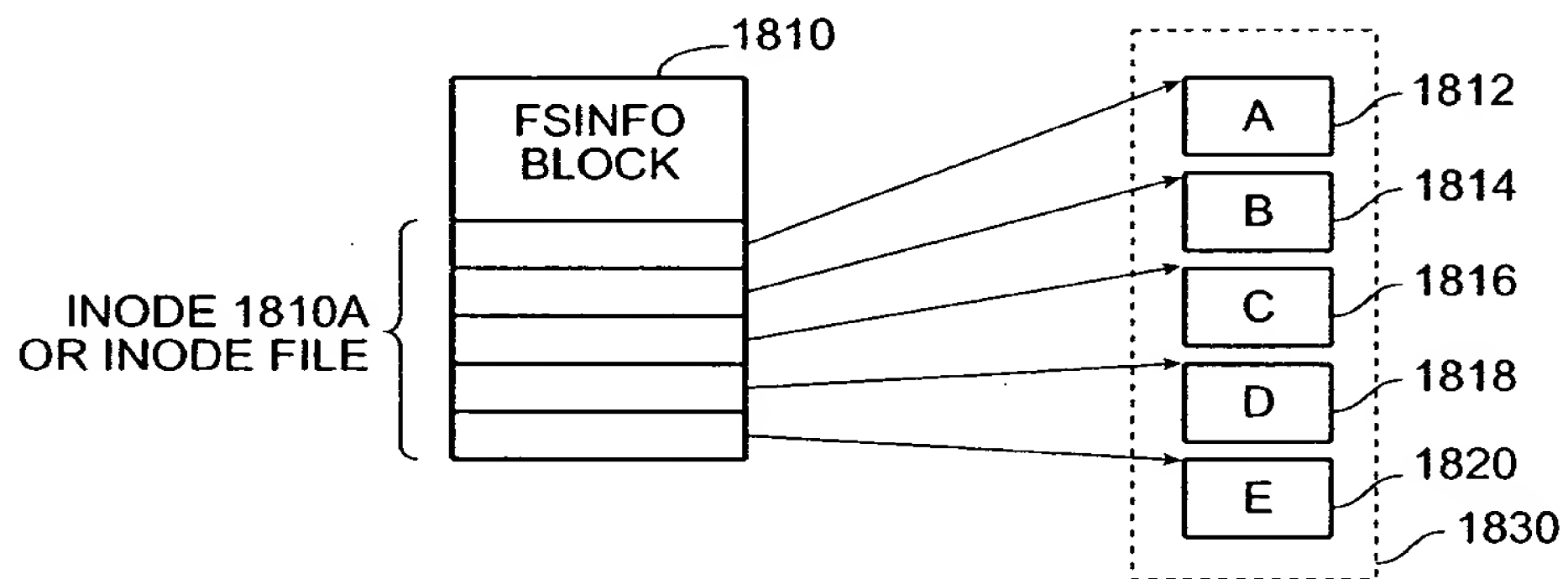


FIG. 18A

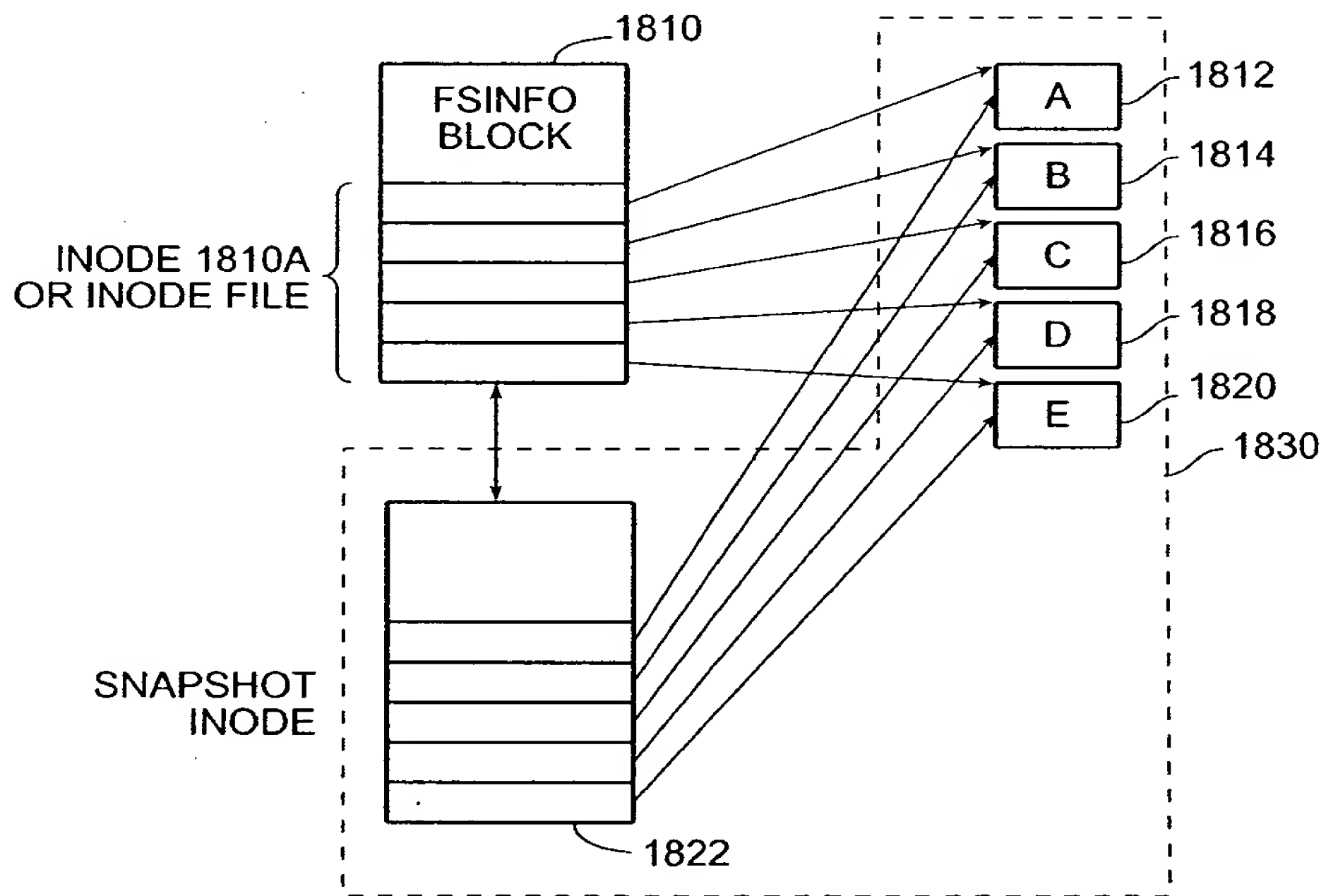


FIG. 18B

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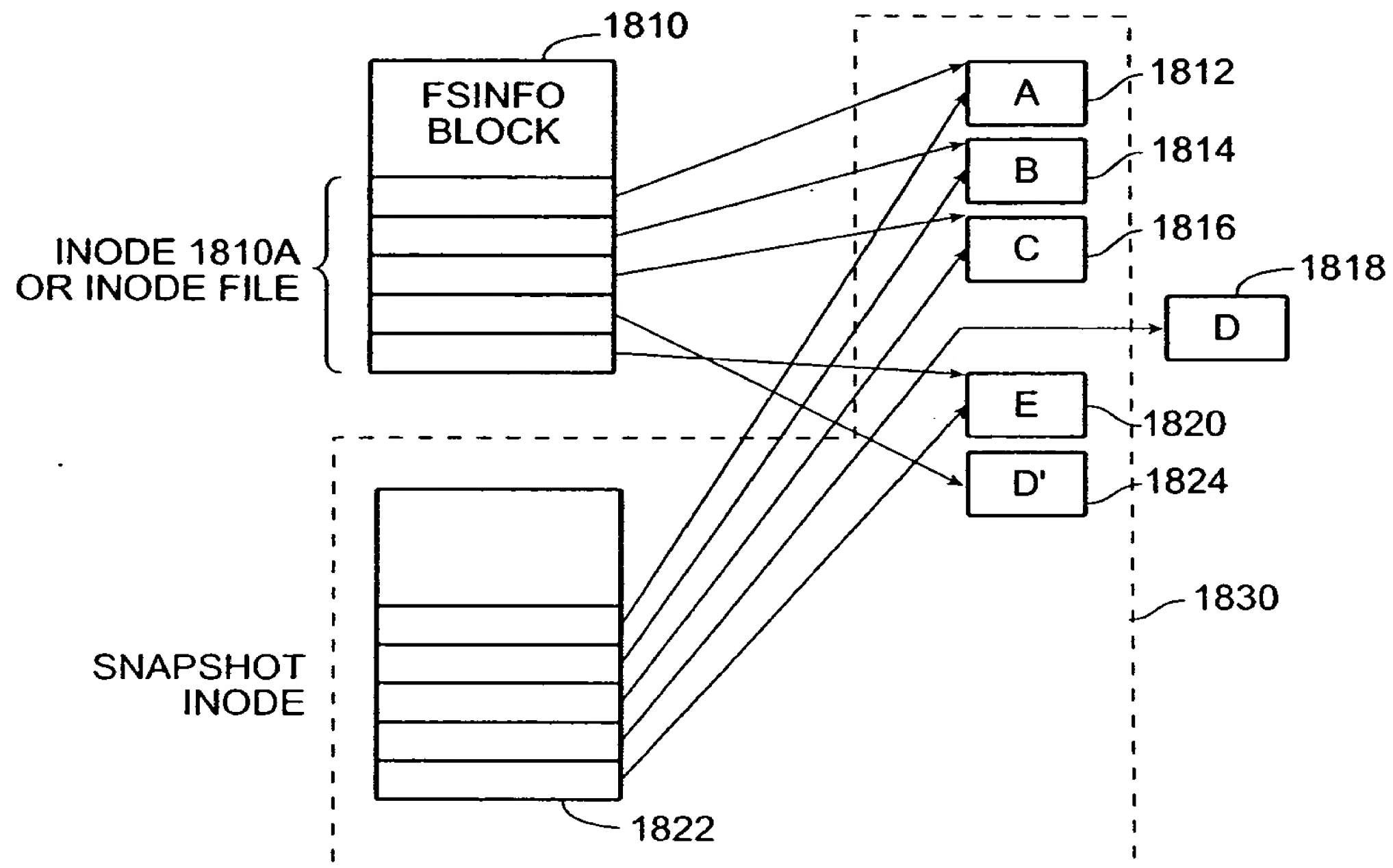


FIG. 18C

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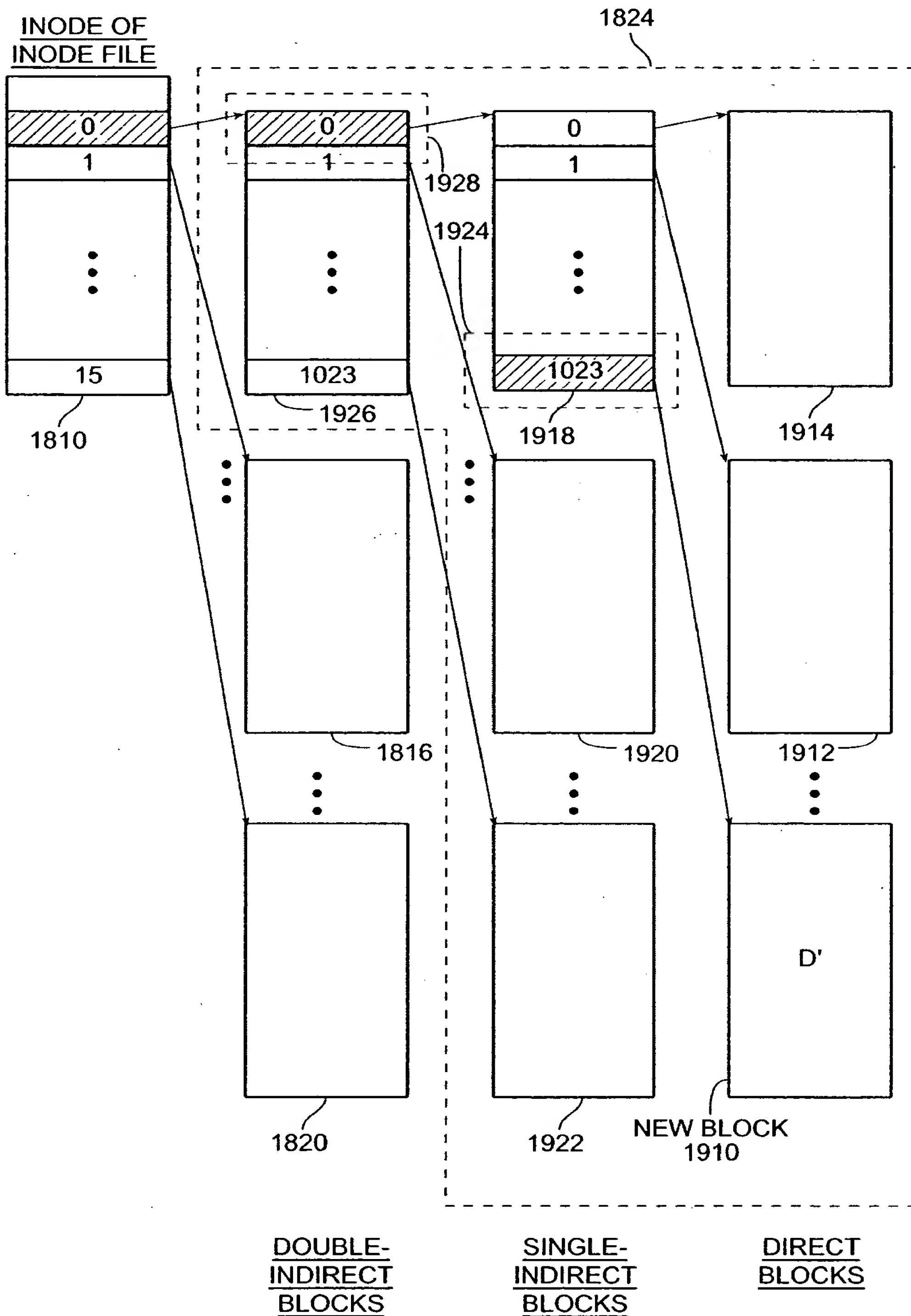


FIG. 19

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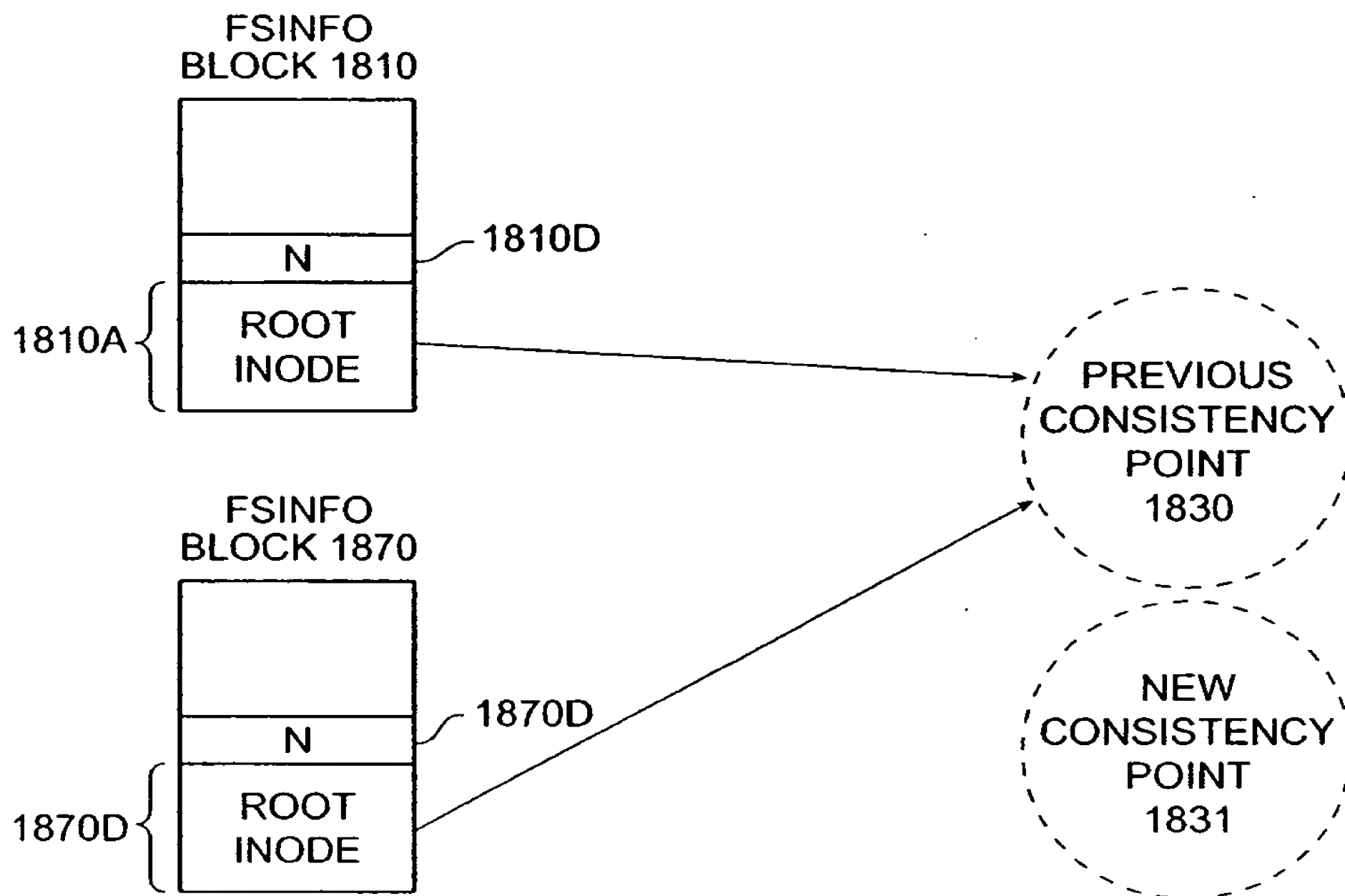


FIG. 20A

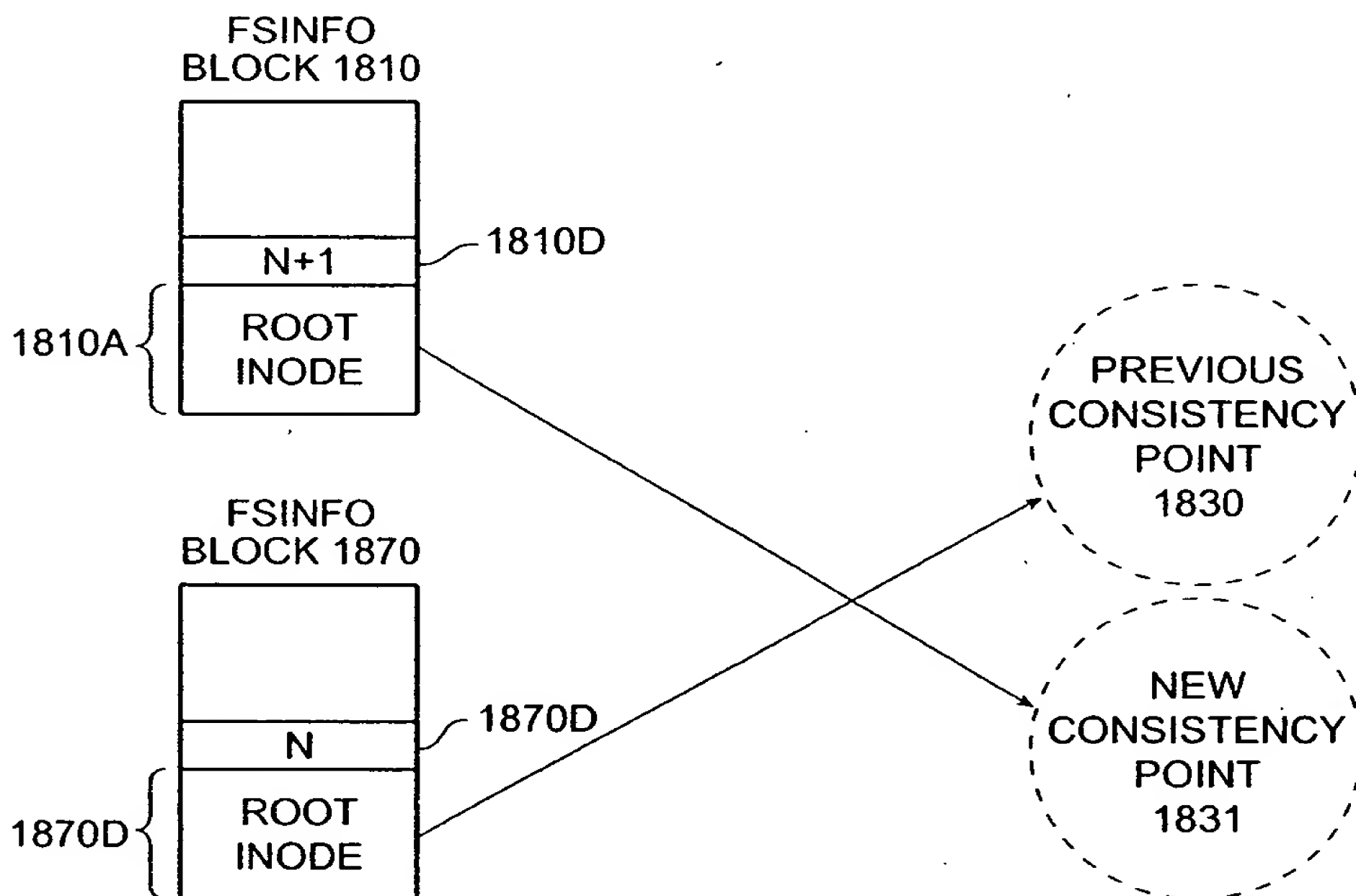


FIG. 20B

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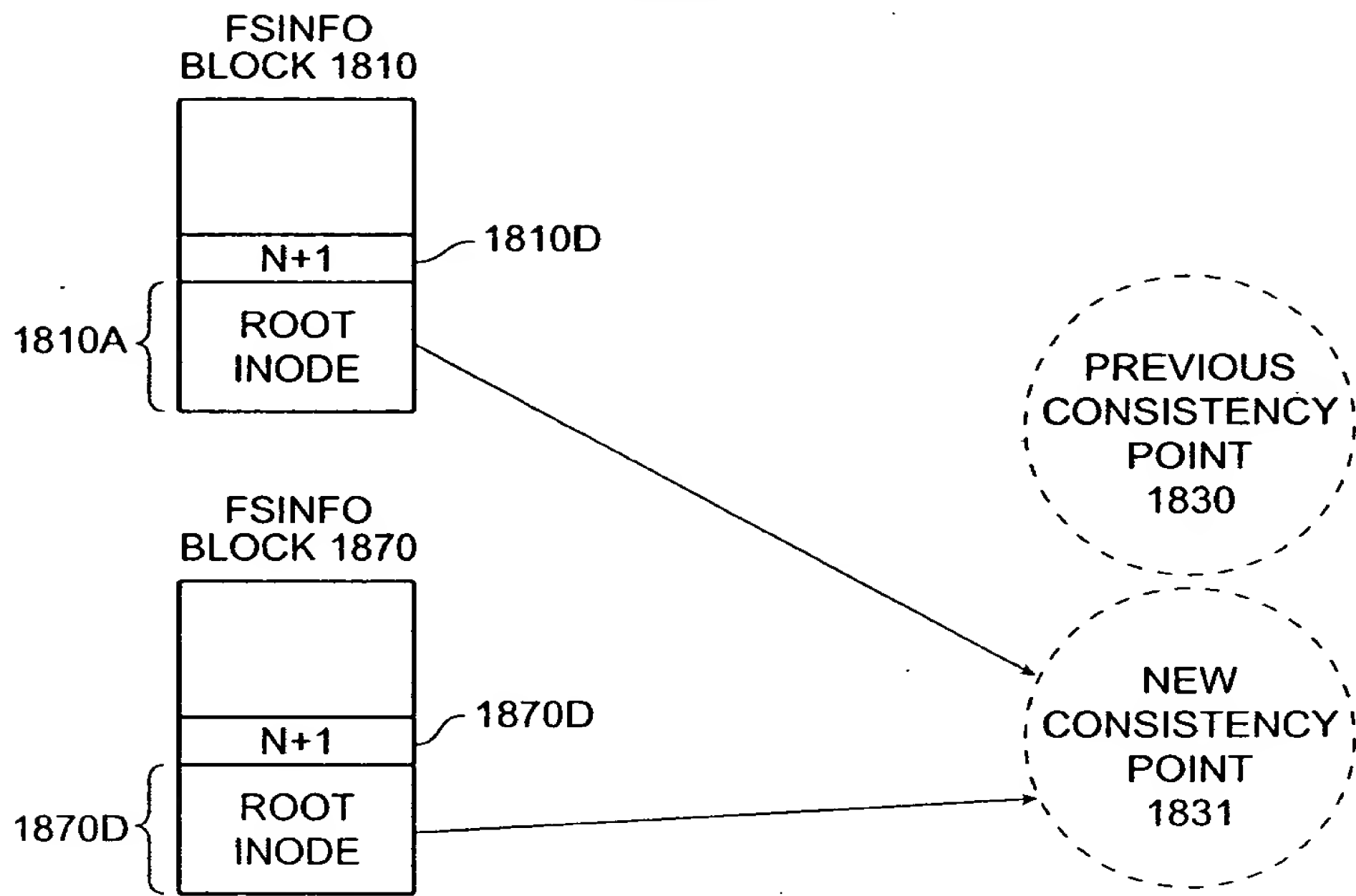


FIG. 20C

SNAPSHOT 2 BIT

BIT 31
(CP-BIT)

BIT 2

BIT 1

BIT 0
(FS-BIT)

BLOCK #	BIT 31 (CP-BIT)	...	BIT 2	BIT 1	BIT 0 (FS-BIT)	
2304	1	...	1	1	1	2326A
2306	0	...	0	1	0	2326B
2308	1	...	1	0	1	2326C
2310	1	...	1	1	1	2326D
2312	1	...	1	1	1	2326E
2314	0	...	0	1	0	2326F
2316	1	...	1	1	1	2326G
2318	1	...	1	1	1	2326H
2320	0	...	0	1	0	2326I
2322	1	...	1	0	1	2326J
2324	0	...	0	1	0	2326K
2326	1	...	1	0	1	2326L
2328		...				
		⋮				

4 KB
BLOCK
2326

FIG. 21E

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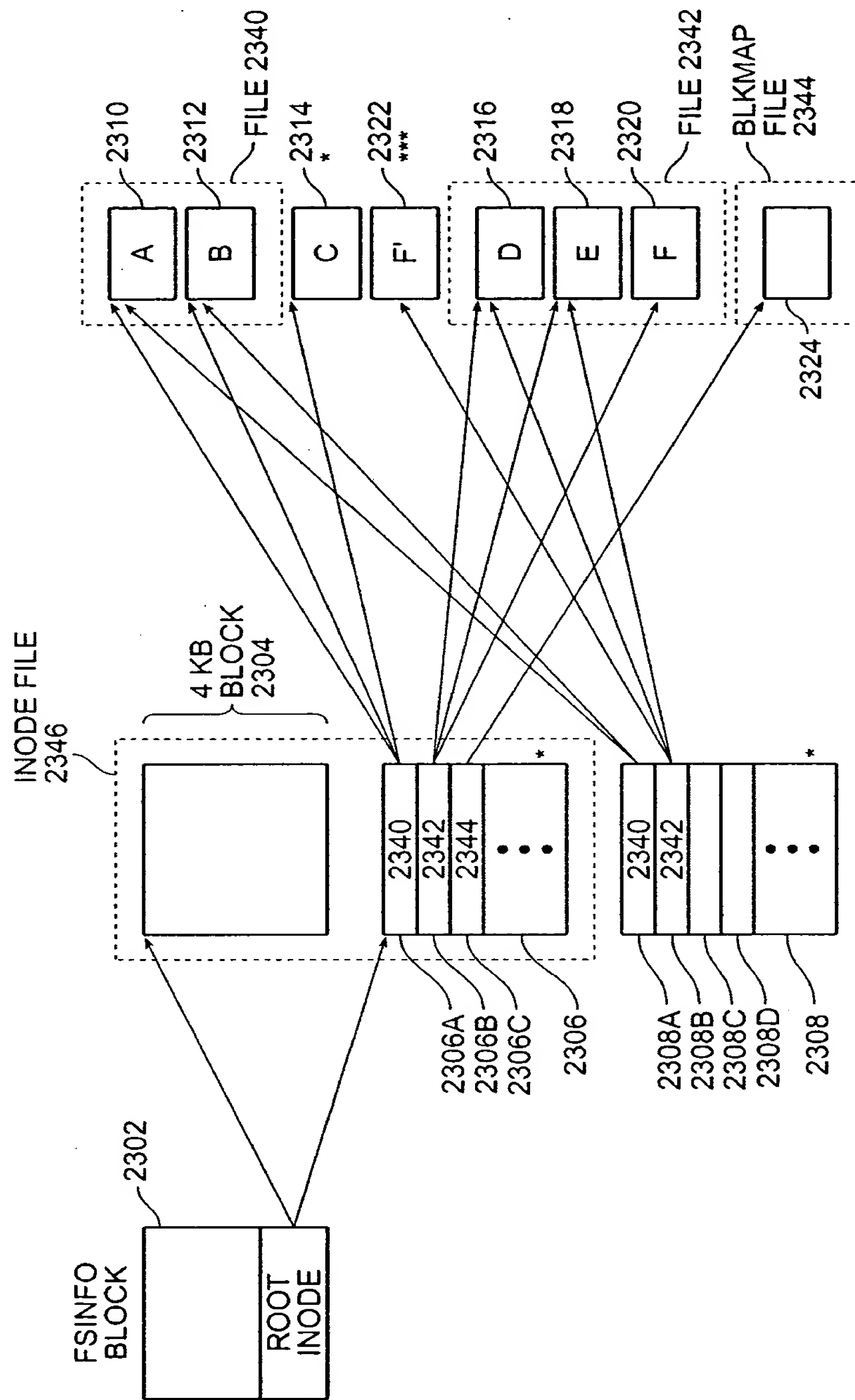


FIG. 21A

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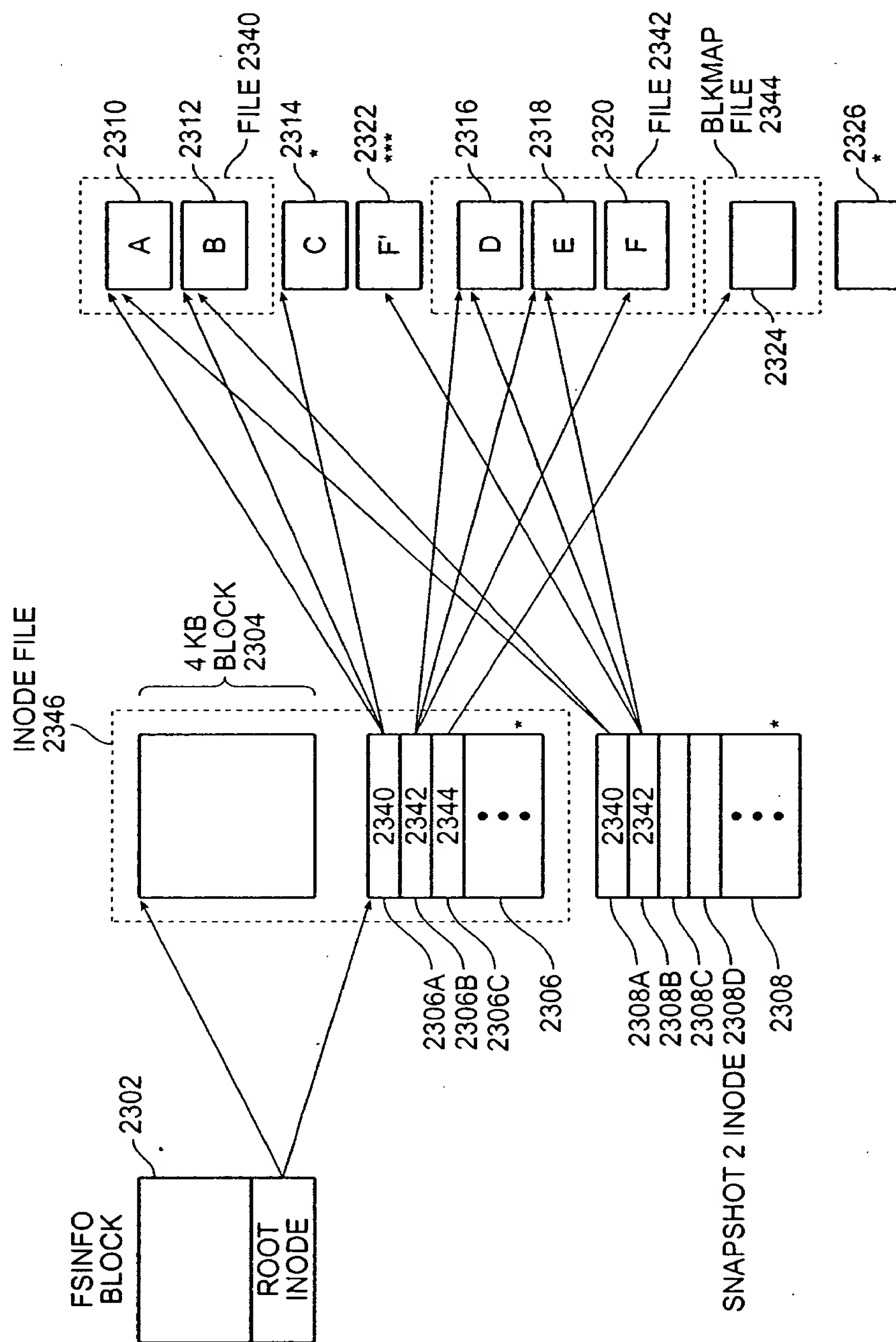


FIG. 21B

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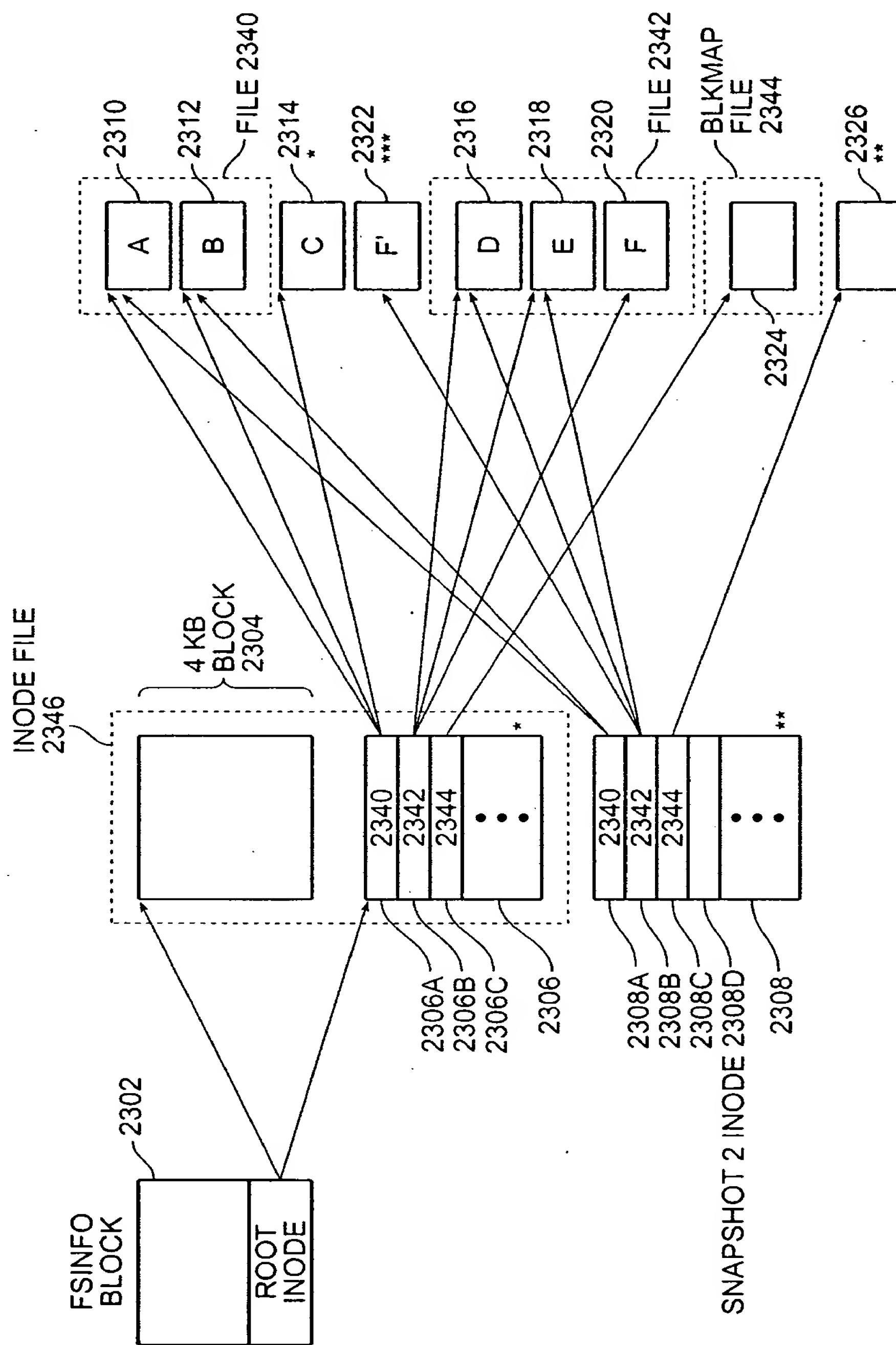


FIG. 21C

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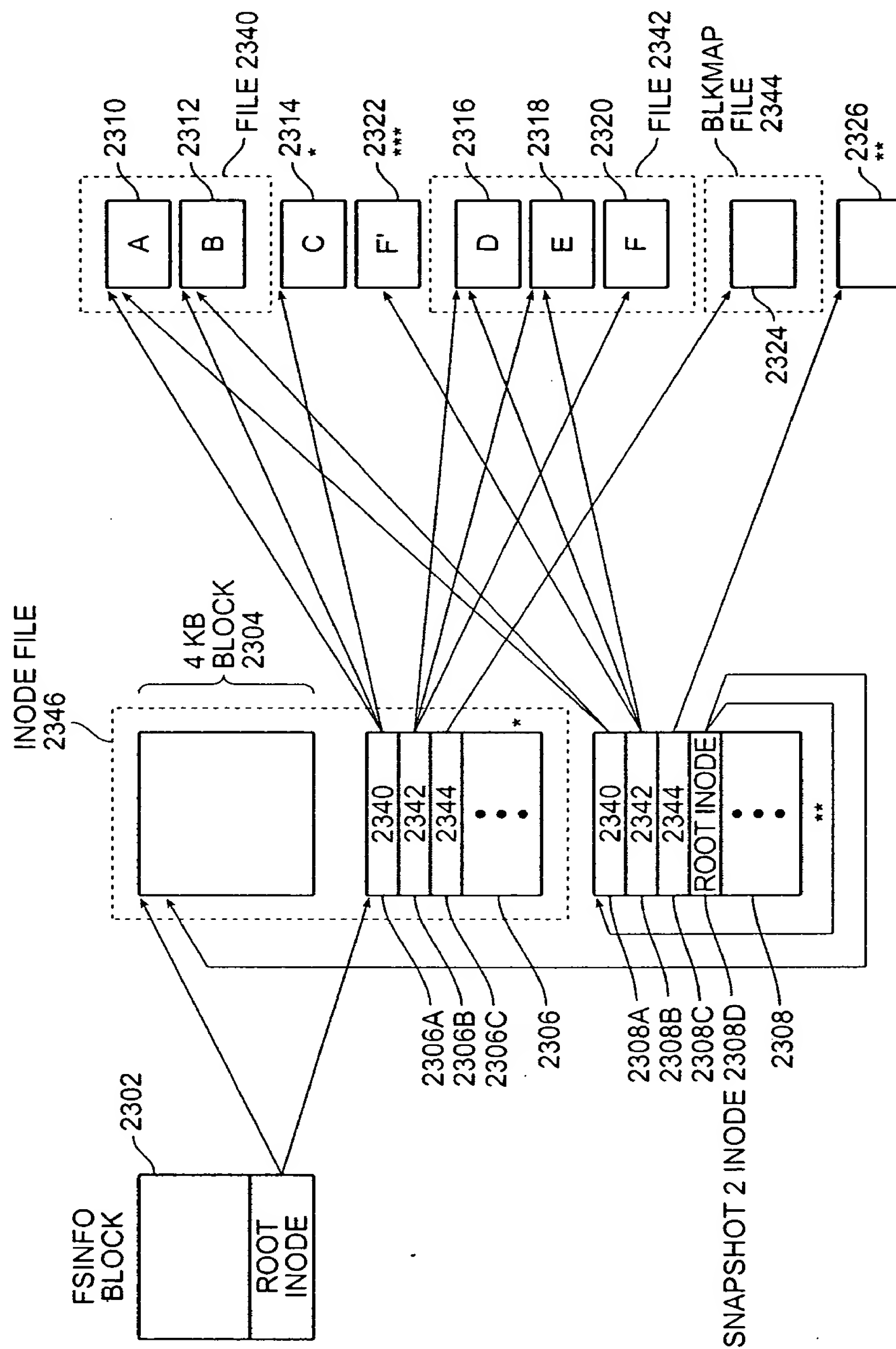


FIG. 21D

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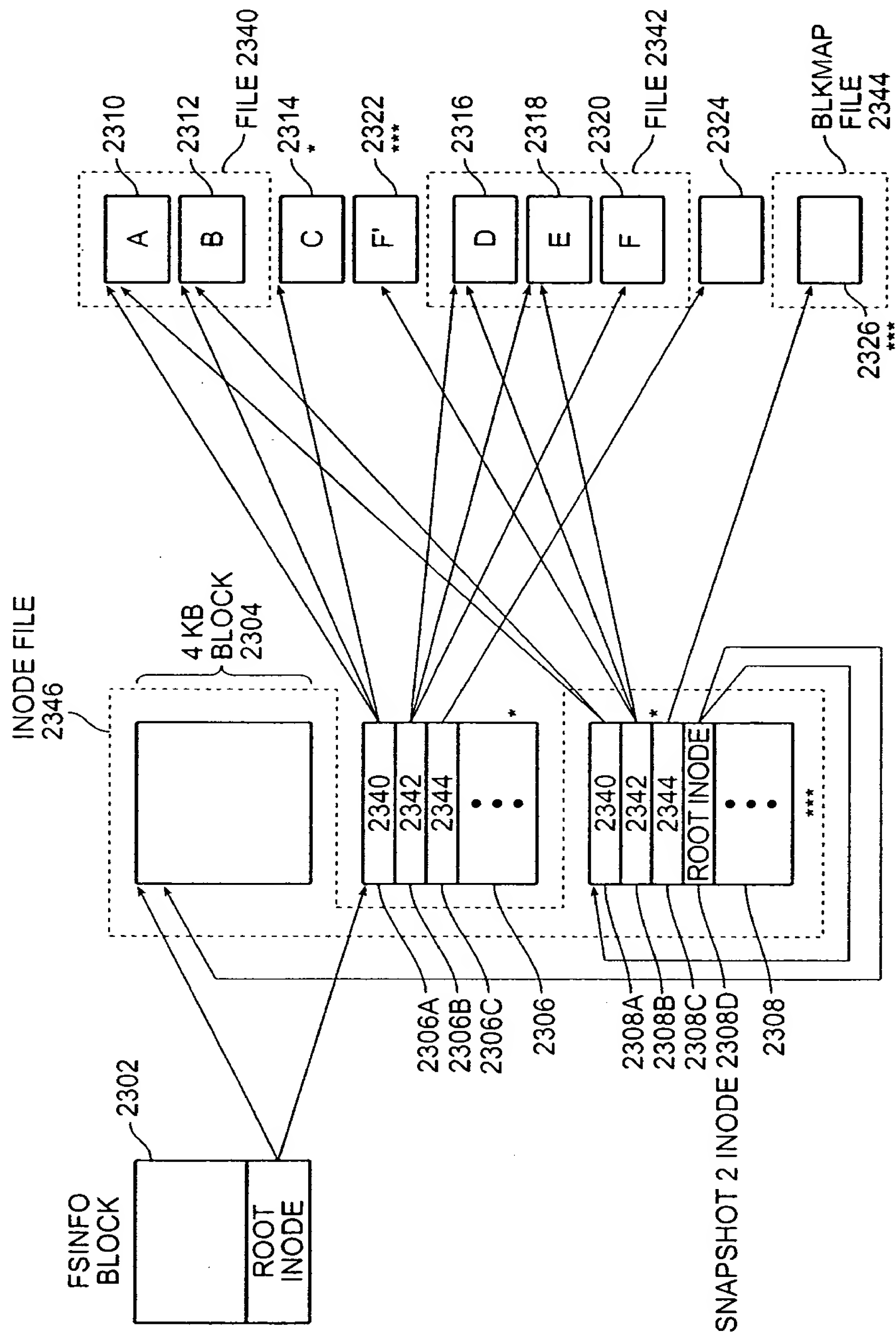


FIG. 21F

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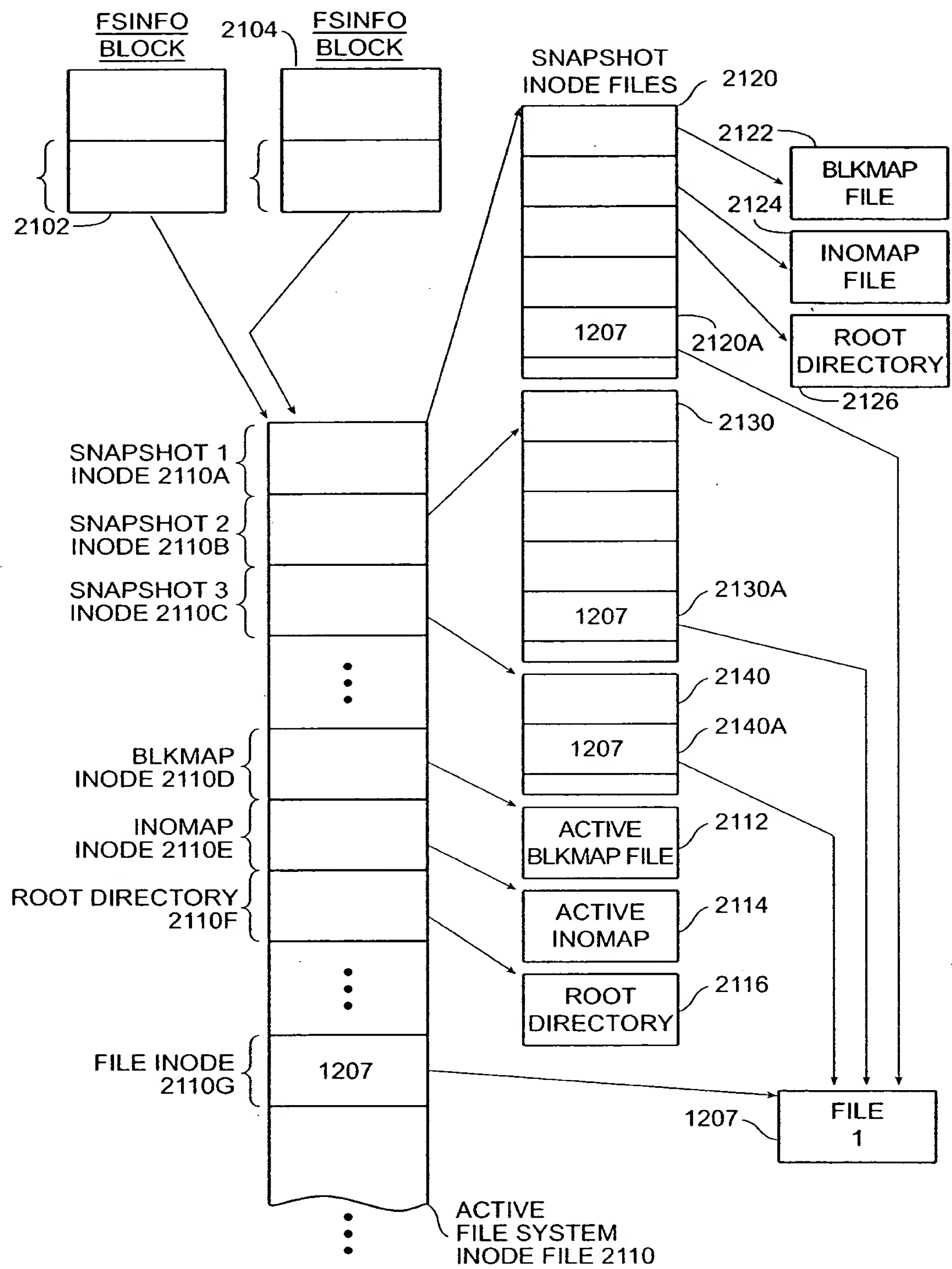


FIG. 22

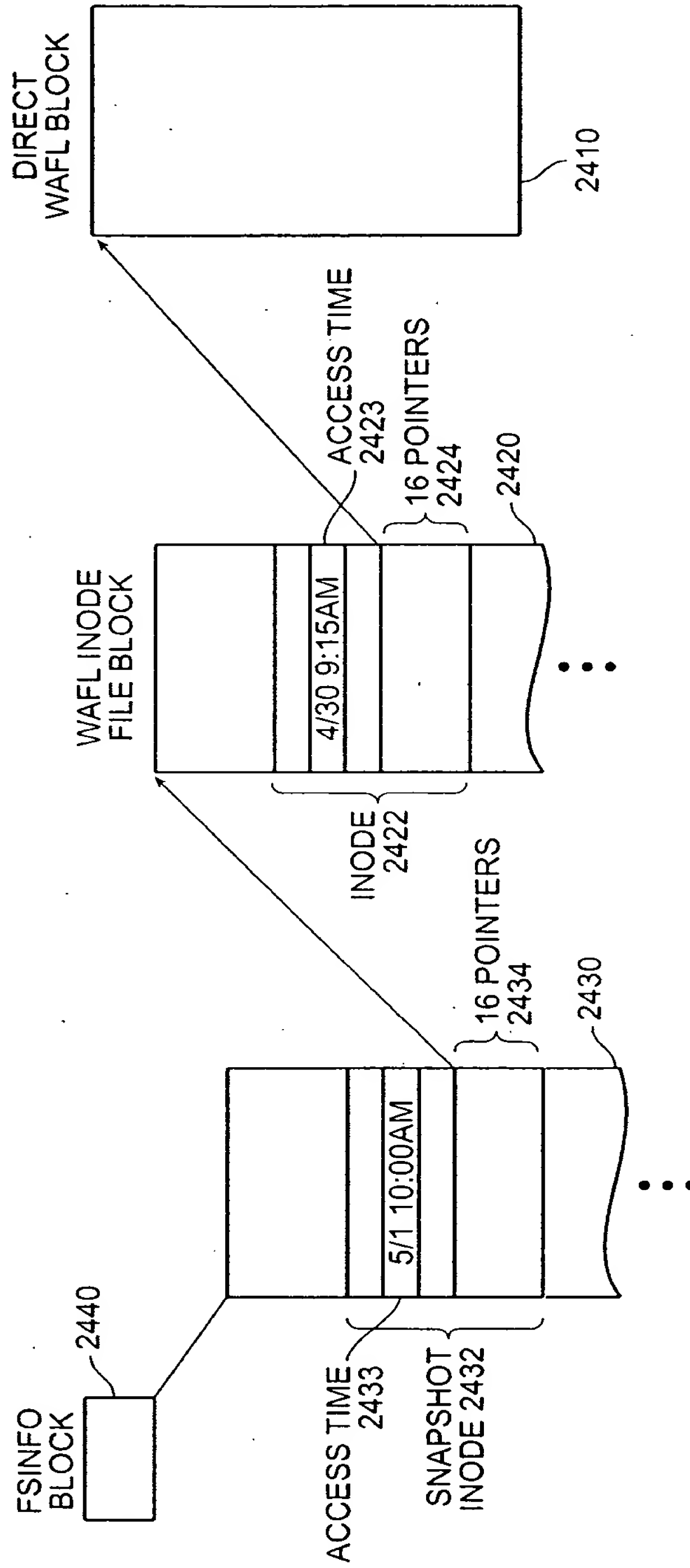


FIG. 23A

